

Electroplating for Electronic Packaging Applications Tutorial

September 2006

CMC Laboratories, Inc.



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Basic Principles of Electroplating



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1. Electrolytic Plating (Electroplating)

- Uses electrical current to reduce metal ions (drive chemical reaction)
- All features need to be electrically connected
- Allows control of the plating process

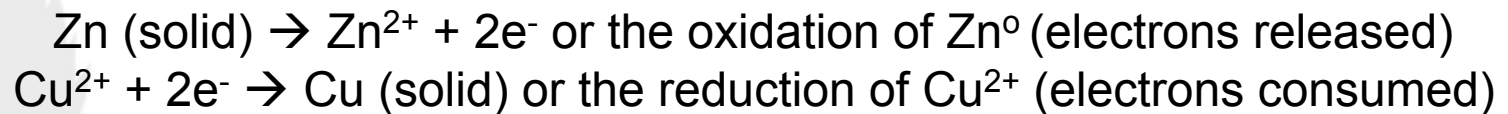
2. Electroless Plating (Autocatalytic)

- Chemical reaction induces metal atom reduction
- Non-conductive substrates
- Features do not need to be electrically connected
- Difficult to control plating parameters
- Limited bath lifetime



3. Immersion Plating (Displacement reaction)

- Metal ion is reduced from solution by exchange with a metal atom from the substrate
- Type of metal deposited must be more noble-higher Electromotive Force (EMF, E_o) than the metal dissolved
- Deposition stops after 10-20 micro inches of deposition



Core Plating Principle: Electroless, Electrolytic, Immersion

Metal Ion Electrons Deposited Metal



Plating Process: Reduction of metal ions from solution

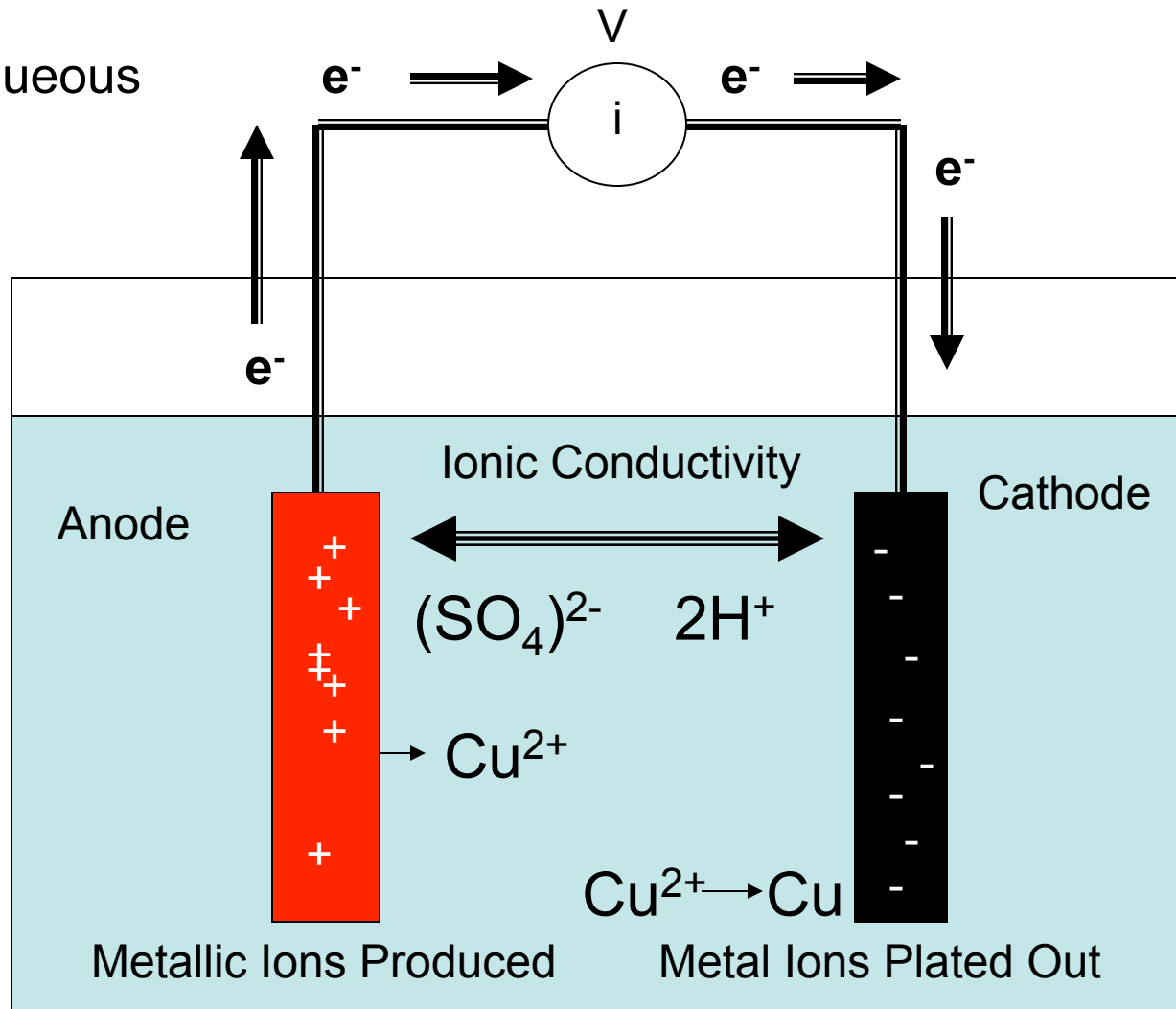
Source for this reduction:

- Electrolytic plating- Applied current
- Electroless plating- chemical reaction in solution
- Displacement plating- dissolution of electrode metal



Electroplating- Electrically Driven REDOX Reactions

H_2SO_4 in an aqueous electrolyte



- Reduction voltage of metals under a standard set of conditions
- Voltages are relative to a standard reaction: 2H^+ (aqueous) + $2\text{e}^- \rightarrow \text{H}_2(\text{gas})$
- More positive E_o , stronger oxidation agent (electron acceptor)
- More positive E_o , more noble metal
- More negative E_o , stronger reducing agent (electron donor)

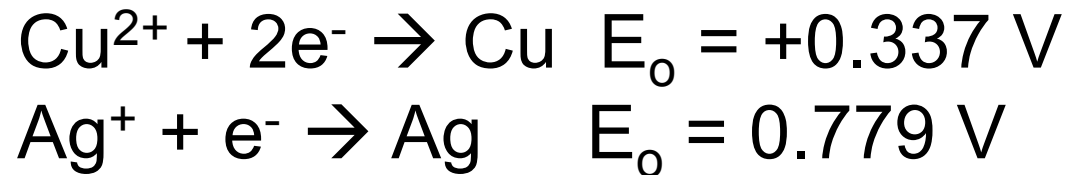


The Electromotive Force Series

<i>Electrode</i>	<i>Potential, V</i>	<i>Electrode</i>	<i>Potential, V</i>
$\text{Li} \rightleftharpoons \text{Li}^+$	-3.045	$\text{Co} \rightleftharpoons \text{Co}^{++}$	-0.277
$\text{Rb} \rightleftharpoons \text{Rb}^+$	-2.93	$\text{Ni} \rightleftharpoons \text{Ni}^{++}$	-0.250
$\text{K} \rightleftharpoons \text{K}^+$	-2.924	$\text{Sn} \rightleftharpoons \text{Sn}^{++}$	-0.136
$\text{Ba} \rightleftharpoons \text{Ba}^{++}$	-2.90	$\text{Pb} \rightleftharpoons \text{Pb}^{++}$	-0.126
$\text{Sr} \rightleftharpoons \text{Sr}^{++}$	-2.90	$\text{Fe} \rightleftharpoons \text{Fe}^{3+}$	-0.04
$\text{Ca} \rightleftharpoons \text{Ca}^{++}$	-2.87	$\text{Pt}/\text{H}_2 \rightleftharpoons \text{H}^+$	0.0000
$\text{Na} \rightleftharpoons \text{Na}^+$	-2.715	$\text{Sb} \rightleftharpoons \text{Sb}^{3+}$	+0.15
$\text{Mg} \rightleftharpoons \text{Mg}^{++}$	-2.37	$\text{Bi} \rightleftharpoons \text{Bi}^{3+}$	+0.2
$\text{Al} \rightleftharpoons \text{Al}^{3+}$	-1.67	$\text{As} \rightleftharpoons \text{As}^{3+}$	+0.3
$\text{Mn} \rightleftharpoons \text{Mn}^{++}$	-1.18	$\text{Cu} \rightleftharpoons \text{Cu}^{++}$	+0.34
$\text{Zn} \rightleftharpoons \text{Zn}^{++}$	-0.762	$\text{Pt}/\text{OH}^- \rightleftharpoons \text{O}_2$	+0.40
$\text{Cr} \rightleftharpoons \text{Cr}^{3+}$	-0.74	$\text{Cu} \rightleftharpoons \text{Cu}^+$	+0.52
$\text{Cr} \rightleftharpoons \text{Cr}^{++}$	-0.56	$\text{Hg} \rightleftharpoons \text{Hg}_2^{++}$	+0.789
$\text{Fe} \rightleftharpoons \text{Fe}^{++}$	-0.441	$\text{Ag} \rightleftharpoons \text{Ag}^+$	+0.799
$\text{Cd} \rightleftharpoons \text{Cd}^{++}$	-0.402	$\text{Pd} \rightleftharpoons \text{Pd}^{++}$	+0.987
$\text{In} \rightleftharpoons \text{In}^{3+}$	-0.34	$\text{Au} \rightleftharpoons \text{Au}^{3+}$	+1.50
$\text{Tl} \rightleftharpoons \text{Tl}^+$	-0.336	$\text{Au} \rightleftharpoons \text{Au}^+$	+1.68



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- Silver ion is easier to reduce than cupric ion
- If both are present in an electrolyte, the silver will plate out first (lower E_o)
- It will take twice the number of electrons to reduce the same number of copper ions as silver (Faraday's First Law)



- Used to determine a relationship between plating conditions and amount of metal deposited
- Valid as long as no side reactions are occurring

$$\text{Weight (grams)} = \{I \text{ (amps)} * t \text{ (seconds)} * E\} / 96,500 \text{ Coulombs}$$

Applied current

Plating time

Chemical Equivalent Weight



What Actually Happens during the Plating Process?

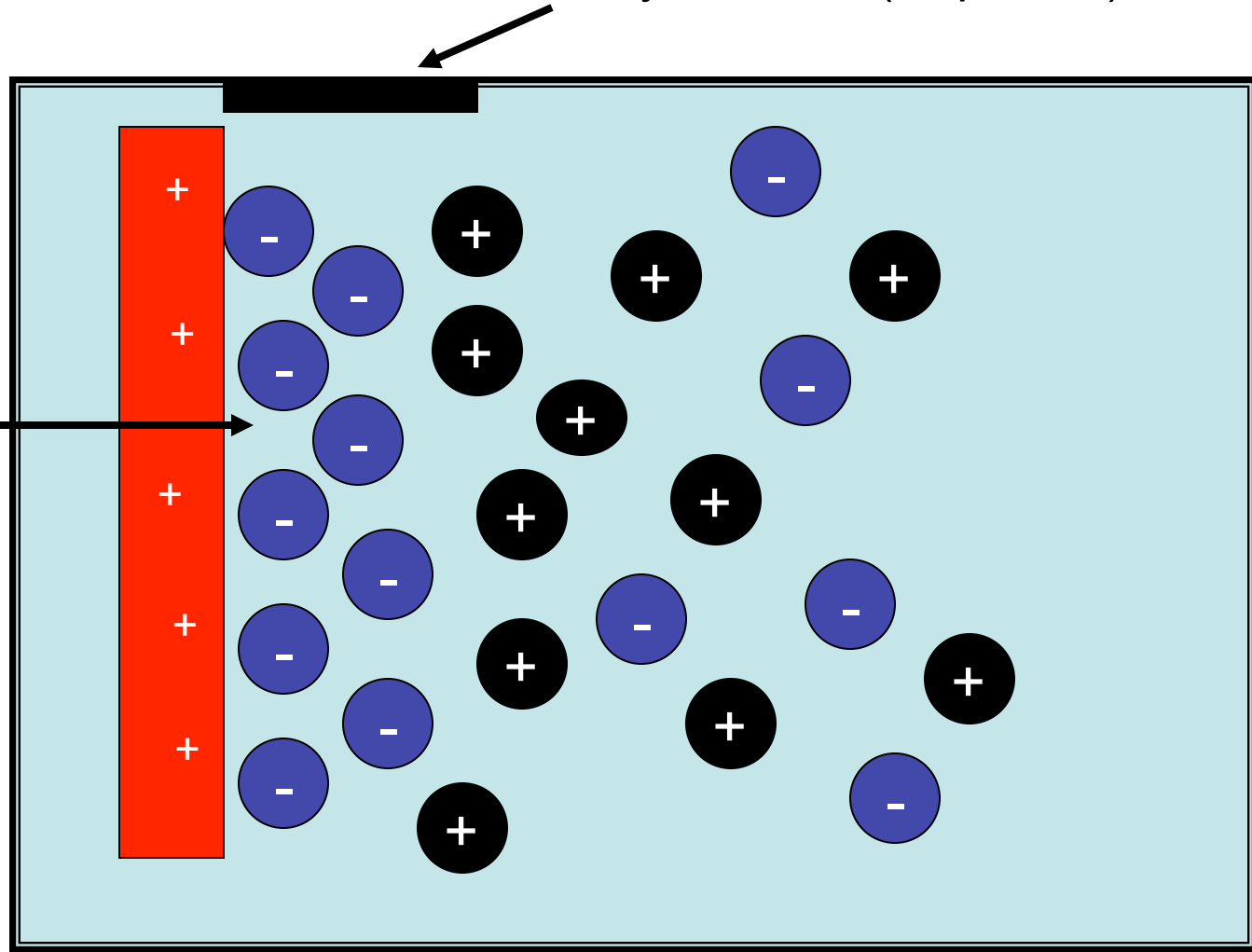


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What Actually Happens During the Plating Process? Electrolyte Dynamics, Charged Electrode

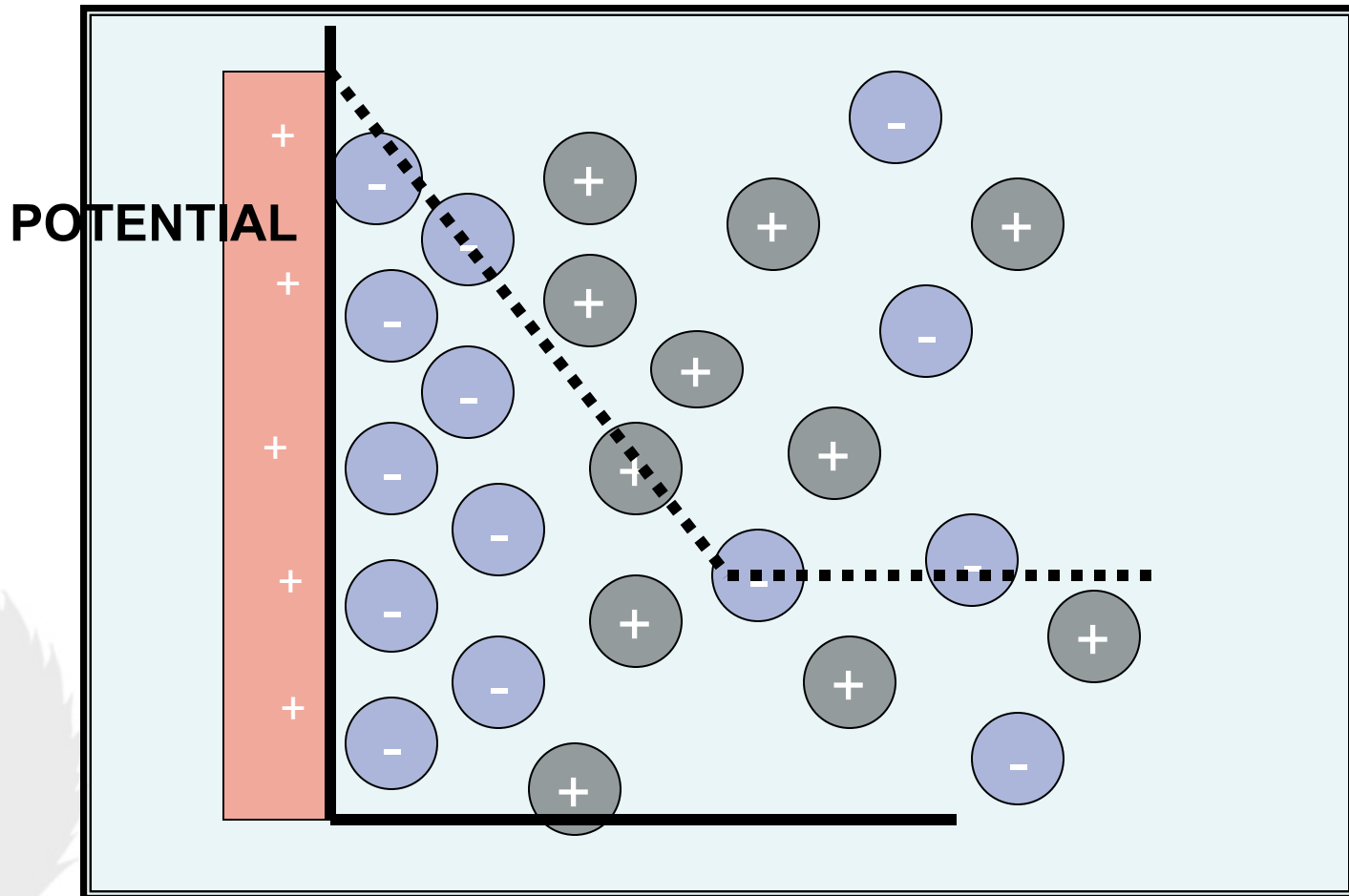
An Electrical Double Layer Forms (Capacitor)

Ions align
in solution
to screen
the applied
potential

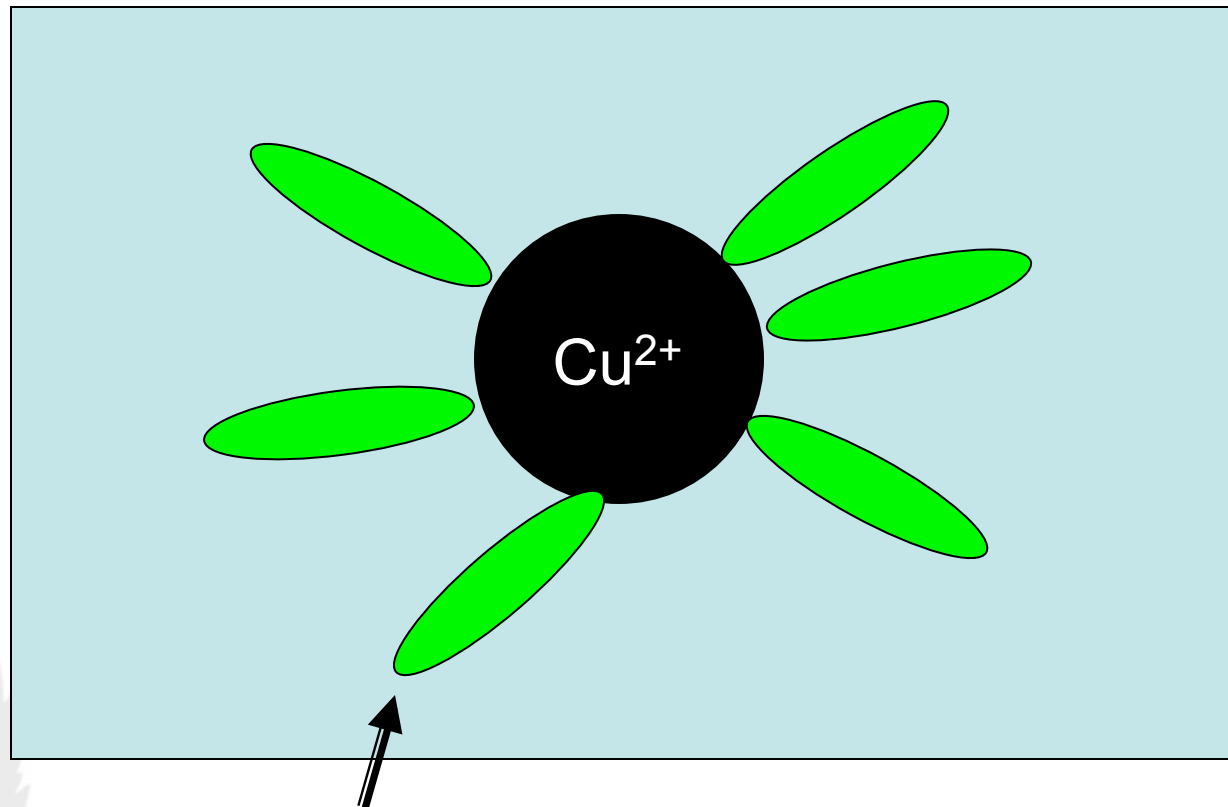
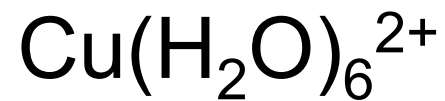


What Actually Happens During the Plating Process? Electrolyte Dynamics, Charged Electrode

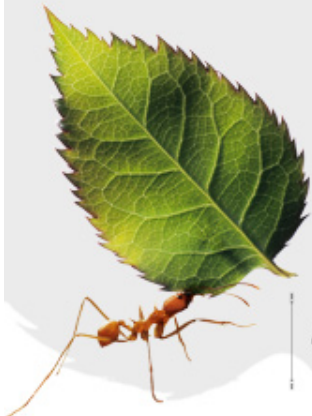
Diffusing Ions must move through the double layer potential to reach the electrode



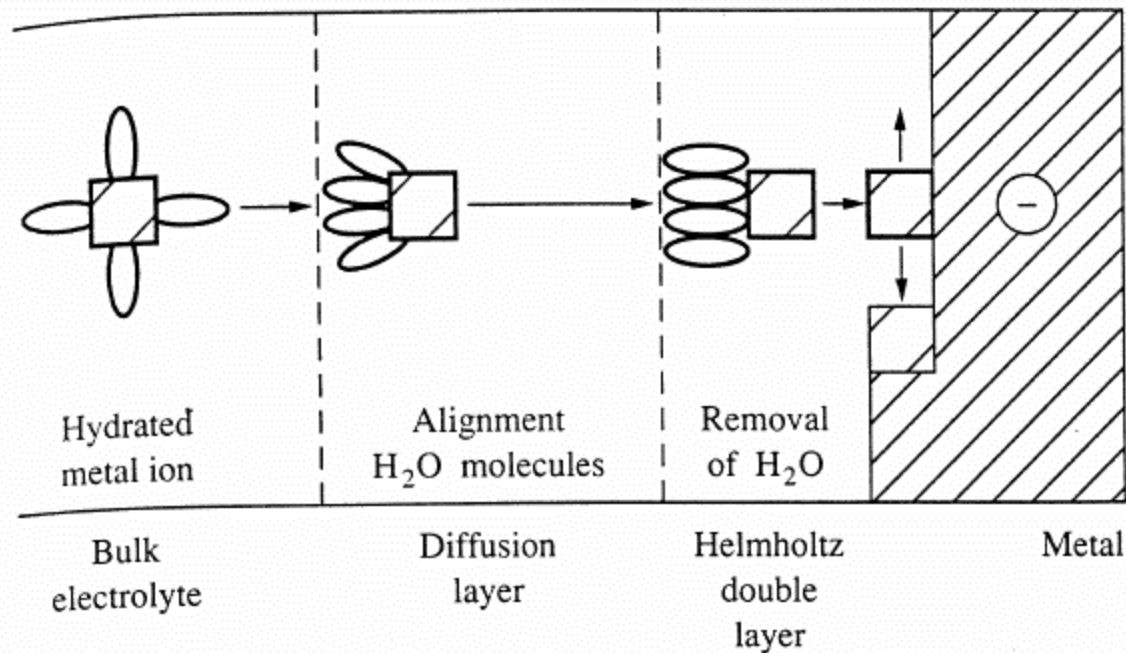
What Actually Happens During the Plating Process? Ions in Solution



Oriented (polar) water molecules- ligands



What Actually Happens During the Plating Process? Diffusion to the Electrode



From Roy Magnuson, Endicott Interconnect (IBM)



What Actually Happens During the Plating Process- Summary and Discussion

- Applied voltage to an electrode results in alignment of ions in solution (to screen out the applied field)
- This produces an electrochemical potential near the electrode surface
- Ions in solution form complexes with polar species (water) or other ligands
- These complexes (ion and ligands) must diffuse through the double layer to react at the electrode surface



What Actually Happens During the Plating Process- Summary and Discussion

- Ligands slow the diffusion of ions through solution
- Removing ligands slows diffusion through the double layer
- The more strongly complexed an ion, the harder it is to reduce the ion
- **pH control is critical to reduce the level of ligands attachment to ions- this is why pH is critical in effecting plating rates**
- **Temperature and agitation will also improve ion diffusion rates**



What Actually Happens During the Plating Process: Metal Deposition Steps- Summary (Cu Example)

1. Transport of hydrated Cu ions in the electrolyte toward the cathode surface
2. Diffusion of the hydrated ions through the double layer- dehydration process
3. Attachment on the cathode surface with charge exchange to become adatoms
4. Migration through surface diffusion of adatoms to nucleation sites
5. Incorporation of adatoms into the lattice to form the copper film



Components of a Plating Bath



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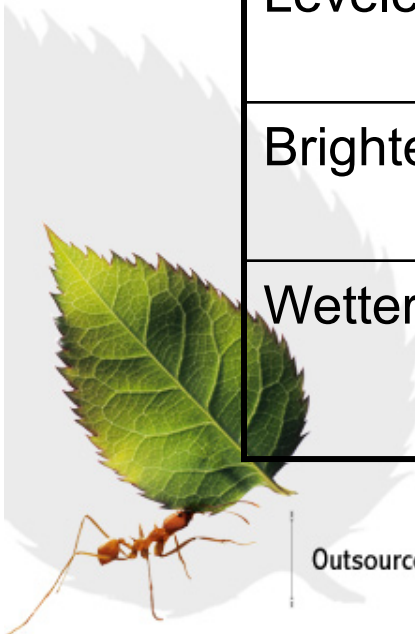
Components of a Plating Bath

<i>Component</i>	<i>Role</i>	<i>Example</i>
Metal Salt	Source of plating metal	Copper sulfate
Acid, Base additive	Provides ionic conductivity in the bath	Sulphuric Acid
Carriers	Help increase throwing power into holes and other surface features	Polyethers or polyoxyethers
Levelers	Improve plating thickness uniformity	Organic nitrogen compounds, amines, amide surfactants
Brighteners	Control deposit brightness and hardness	Sulphur containing compounds
Wetters	Decrease surface tension in solution	Surfactants



Components of a Plating Bath- Mechanism for Additives to Control Properties

<i>Additive</i>	<i>Mechanism (How it Works)</i>
Carriers	Adsorbs on the cathode surface during plating to form a monolayer film. Forms a barrier to diffusion of Cu^{2+} to the surface and suppresses plating rate. Replaced by accelerators (brighteners) in vias and other features.
Levelers	Adsorb strongly to the surface at high current density regions. This lowers the plating rate at corners and protrusions and levels the plated layer.
Brighteners	Attach to the copper metal ions during plating and facilitate charge transfer at the electrode. Brighteners accelerate plating rates and also control grain structure and deposit characteristics.
Wetters	Surfactants with lower the surface tension of the plating solution allowing better wetting of the electrolyte at the electrode. Reduces the incidence of bubbles and allows the plating bath to penetrate features such as vias.



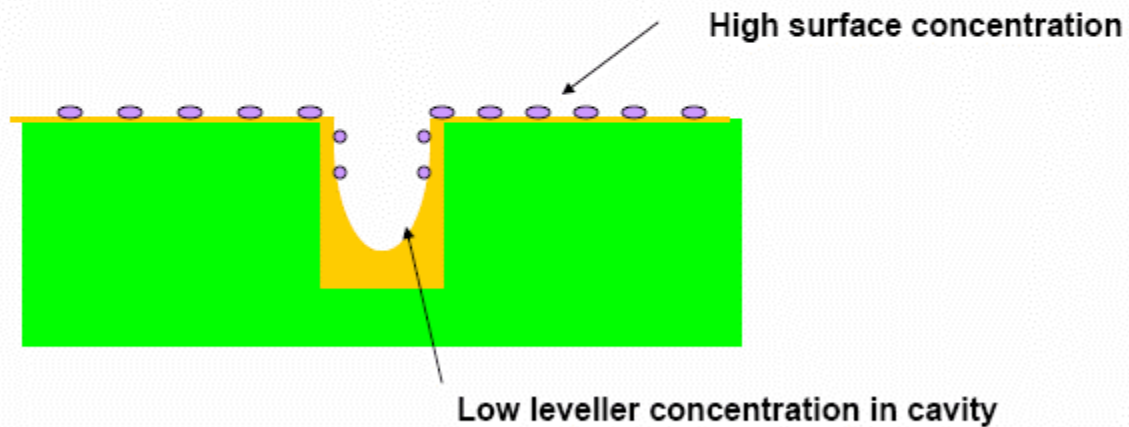
Components of a Plating Bath- Mechanism for Additives to Control Properties

- Goals for the inorganic additives
 - Acid, base concentration optimized for good solution conductivity
 - Metal ion concentration sufficient to support plating current requirements
- Goals for the organic additives
 - Levelers attached to high current density surface features
 - Carrier attached to exposed surface regions
 - High concentration of brighteners in occluded features such as vias
 - Result is higher plating rates in vias, lower plating rates on high current density features, moderate plating rate on exposed surfaces

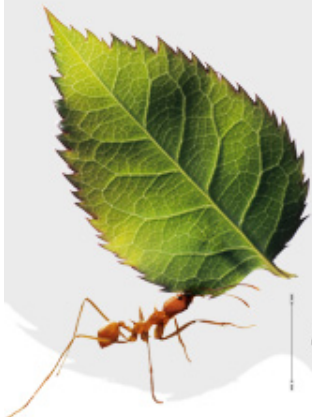


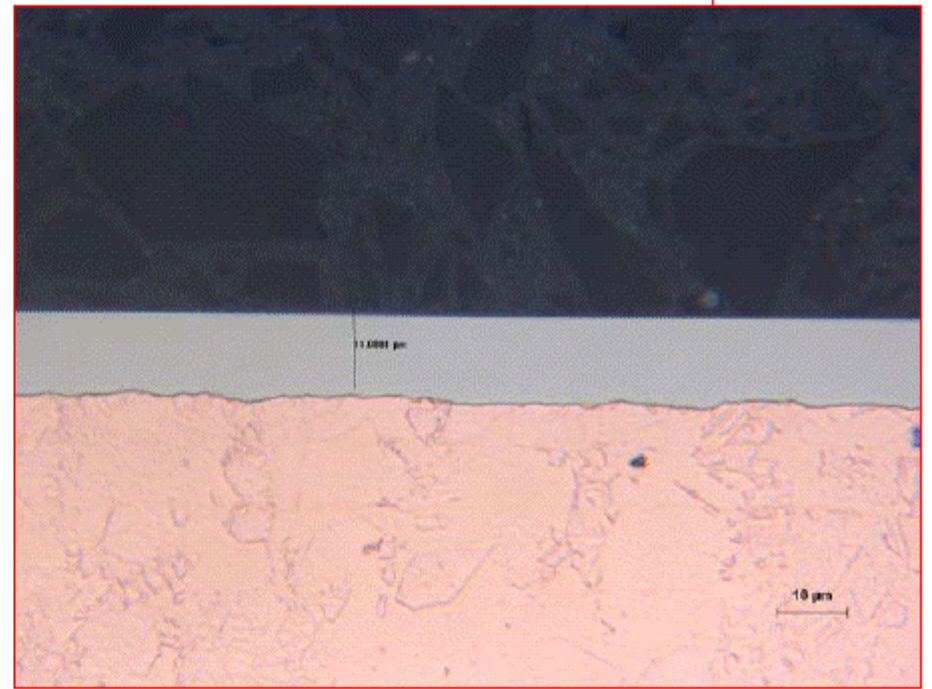
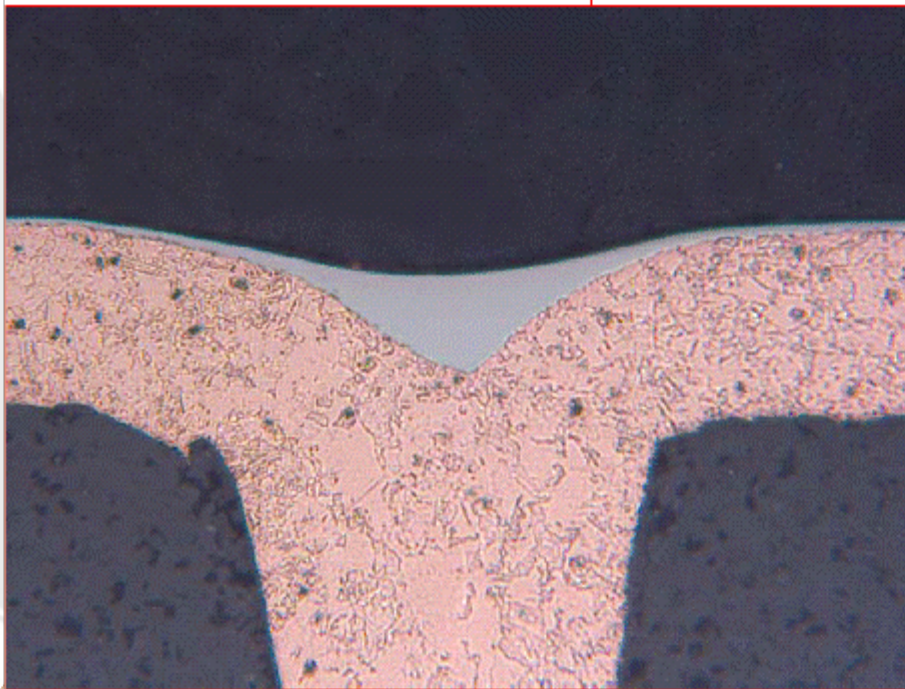
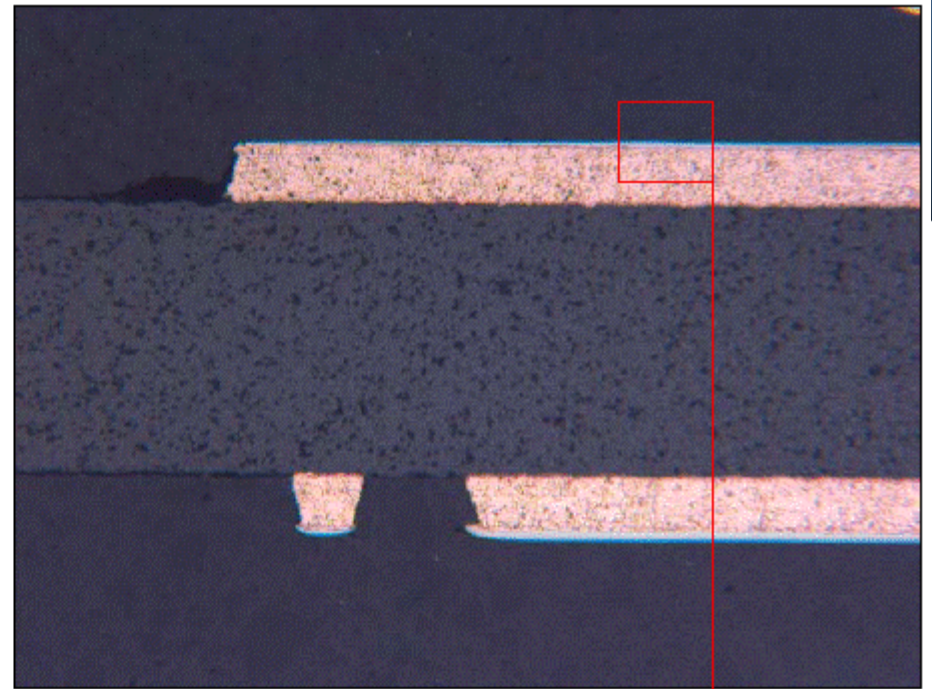
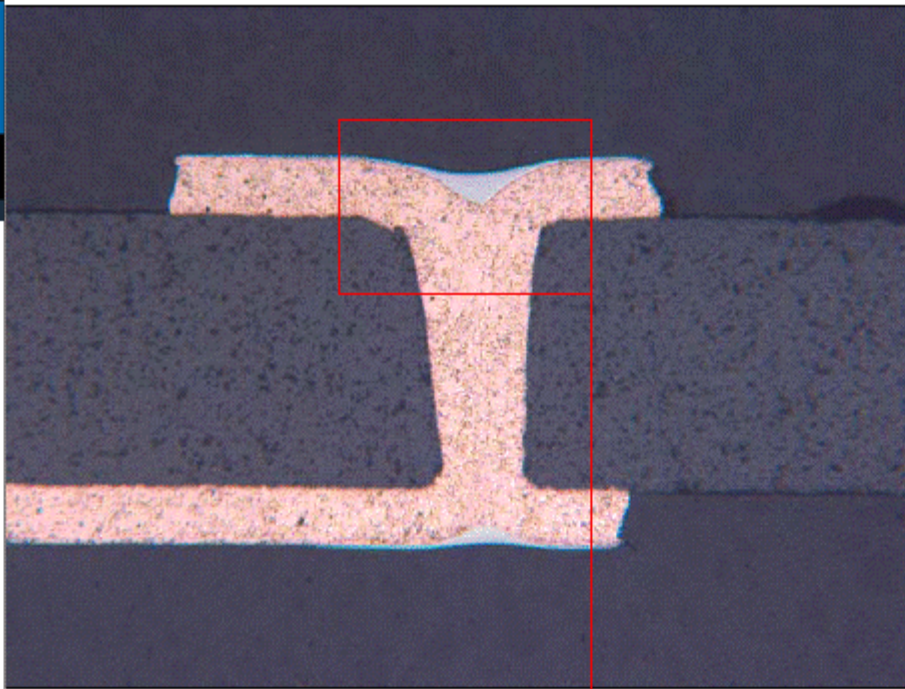
Components of a Plating Bath- Leveler Mechanism Schematic

Strongly adsorbing leveller, suppressing rate at surface



P. M. Vereecken, R. Binstead, H. Deligianni, P. Andricacos,
IBM J. Res. & Dev., 49, 2005, pp3-18.





Rectifier Cycle Effects

- Pulse plating- Grain Size Control
- PPRP in combination with bath additives-
Via Fill Review



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Voltage Cycle Effect on the Plating Process and Plated Deposit

- Goals for the plating cycle
 - For complex surfaces (vias, dense metal), facilitate the optimum organic distributions
 - Brighteners in vias
 - Carriers on the surface
 - Levelers on high current density feature
 - Control the deposit grain structure (with grain refining additives)
 - Optimize the plating rate
- DC Plating
- Pulse Plating
- Pulse Reverse Plating



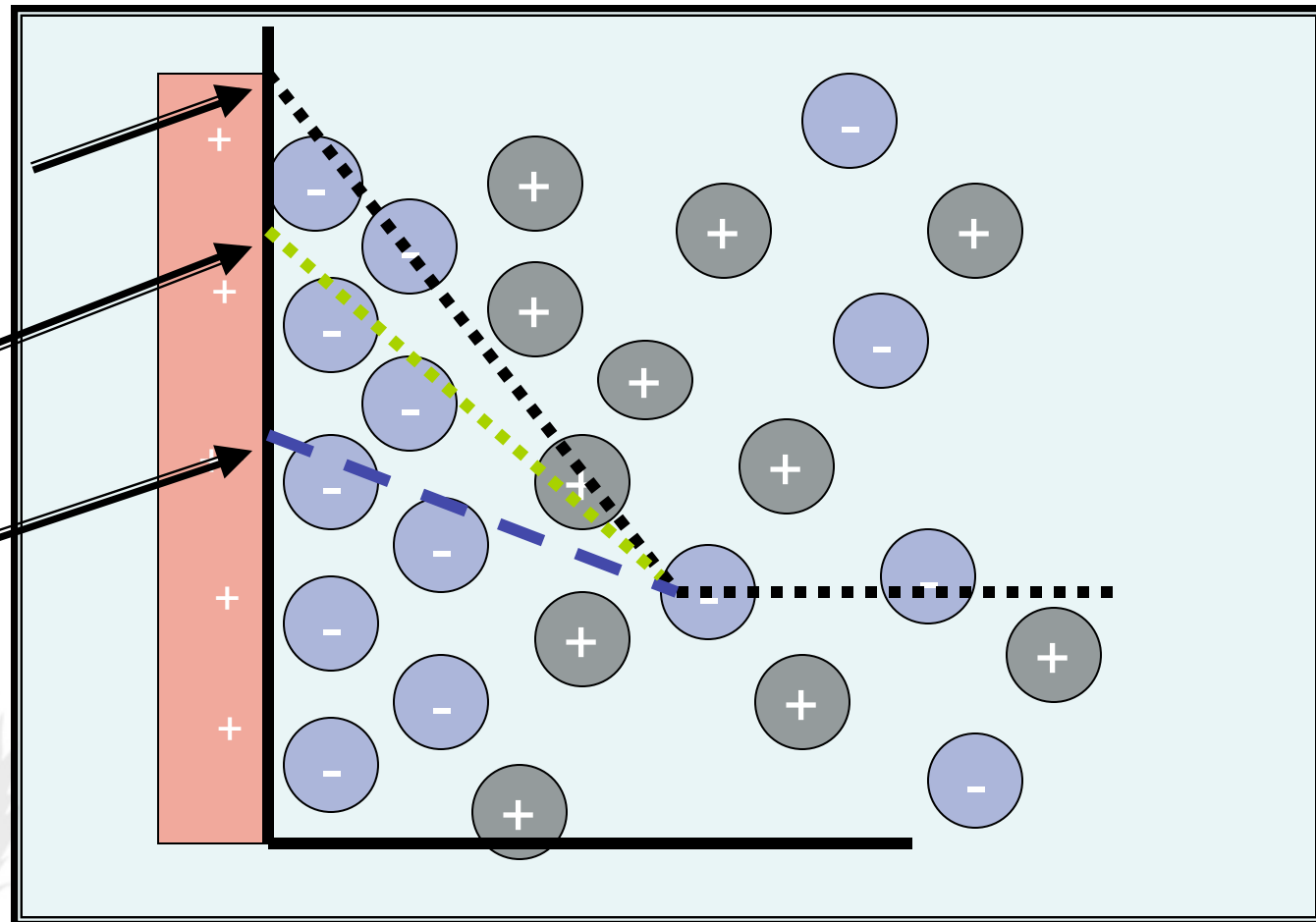
Diffusing Ions must move through the double layer potential to reach the electrode

POTENTIAL

DC Plating

Pulse Plating
Pulse ON

Pulse Plating
Pulse OFF

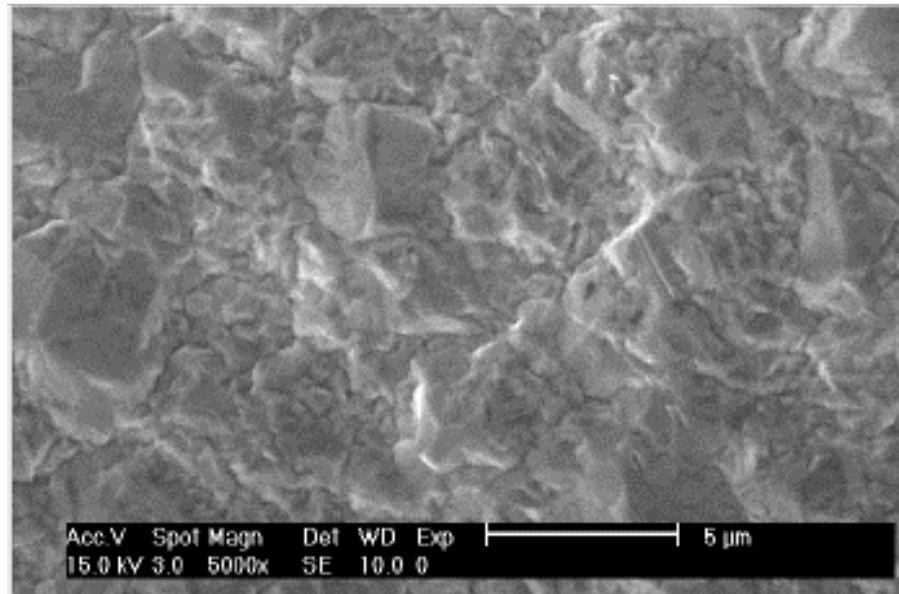


Parameter	Plating Impact
<p>Relaxation after pulse lowers ionic potential at electrode surface- allows more effective diffusion of plating species to electrode surface.</p>	<p>Increased plating rate in diffusion controlled situations and increased plating thickness uniformity.</p>
<p>During each pulse, deposition and film nucleation is initiated.</p>	<p>Controlling the pulse frequency allows control of the nucleation rate and thus controls the grain growth dynamics, size and orientation.</p>



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Pulse Plating: Au Grain Structure Example



Pulse plating is used to create large grained gold (to inhibit Ni diffusion during die attach)



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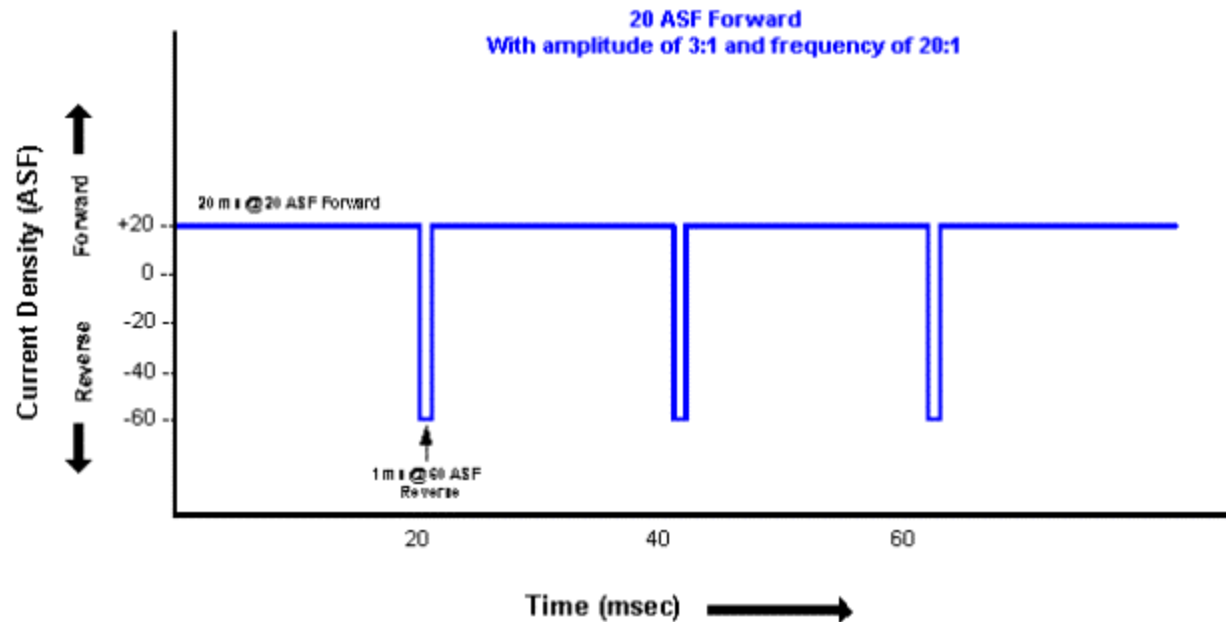
Periodic Pulse Reverse Plating for Enhanced Via Fill (PPR)

- Long duration forward current (substrate is the cathode) followed by short duration reverse current (substrate is the anode)
- Typical 20ms forward/ 1ms reverse (20:1)
- Reverse current is typically 3 times higher magnitude than the forward current (1:3)



Periodic Pulse Reverse Plating for Enhanced Via Fill

Typical Pulse Waveform



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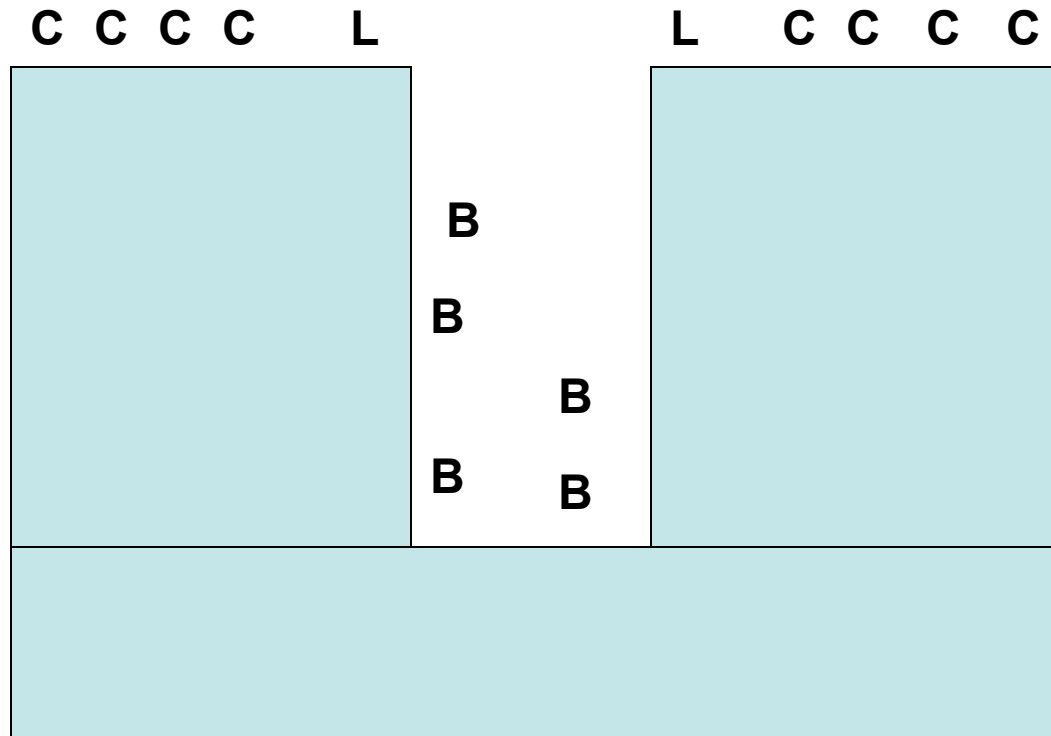
1. Initially the Carrier (plating suppressor) is uniformly absorbed on the surface of the substrate
2. During I_f Brightener is absorbed on the surface of the substrate and in the through holes with the concentration proportional to the (local) current density
3. During I_R Brightener desorbs from both the surface and the via holes with the amount of desorption proportional to current density → More desorption from the surface than in the vias
4. Due to high mass transport at the surface- the desorbed brightener is easily washed from the surface region but remains concentrated in the (more stagnant) via holes
5. Result: Higher brightener concentration in the via holes which enhances the via hole plating rate



PPR Plating for Via Fill Organic Distribution

Schematic Showing the Dominant Organic species in each location

Agitation



PPR- Different Plating Environment in Different Parts of the Substrate!!

PPR Plating and Via Fill- Summary Points

- Very effective in filling high aspect ratio open or blind vias
- Creates different plating environments in different regions of the substrate by manipulating the surface concentration of organic additives
- Agitation is critical to remove Brightener from surface regions
- In-Via agitation can disrupt the required Brightener concentration in via holes
- The plating bath **MUST** be designed for PPR Plating



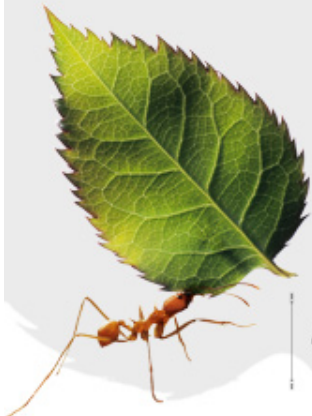
Other Critical Factors for Effective Cu Via Fill

- Ohmic resistance through the length of a high aspect ratio Via is a major obstacle to uniform plating
- Ohmic resistance is minimized by *increasing* the ratio of H_2SO_4 / Cu or increasing temperature

(1) High throw Cu bath for electronic applications

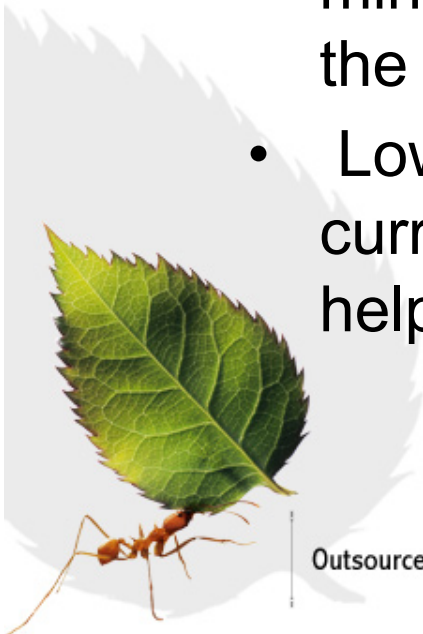
(2) Bath designed for filling high aspect ratio vias

<i>Property</i>	<i>Cu Bath M (1)</i>	<i>Electro-deposit 1000(2)</i>
CuSO4 (g/L)	68	25
Cu (g/L)	17	6
H2SO4 (g/L)	173	225
H2SO4:Cu	10:1	38:1
Current ASF	20-40	10-20



Other Critical Factors for Effective Cu Via Fill

- IR drop through via structure leads to poor via fill
 - Thickness of seed layer along the via walls
 - Thickness of substrate
 - Plating current density
- Via size must be wide enough to sputter minimum of 1000 angstroms of seed metal along the wall
- Low current density cycle followed by higher current density (once the wall is plated) can also help with this issue



Other Critical Properties of the Plated Layer and Plating Factors Which Effect These Properties

1. Plating Thickness Uniformity
2. Surface Morphology and Texture
3. Mechanical Properties



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Other Critical Properties: Plating Thickness Uniformity



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- Differences in plating potential change the plating rate (and resultant plating thickness)
- $\Delta E_t = \Delta \eta_{ct} + \Delta \eta_{mt} + E_{IR}$
 - $\Delta \eta_{ct}$ represents differences in charge transfer rates (kinetics)
 - $\Delta \eta_{mt}$ represents differences in mass transfer (solution flow and diffusion)
 - E_{IR} represents IR drop differences



Plating Thickness Uniformity- Fundamentals

- Key Goal: Minimize ΔE_t
 - Within a substrate
 - From substrate to substrate on a rack
 - From rack to rack within a shift
 - From week to week
- Each of these areas requires control of different parameters



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Critical Factors for Thickness Control

<i>Factor</i>	<i>Influence</i>	<i>Comments</i>
Electrolyte conductivity/ metal concentration	E_{IR}	The higher electrolyte conductivity/ metal concentration ratio decreases ohmic resistance in the electrolyte
Lateral Flow	$\Delta\eta_{ct} \Delta\eta_{mt}$	Controls reacting species concentration across the substrate. Effects charge transfer by controlling additive distributions.
Cell Configuration and Rack Design	$\Delta\eta_{mt} E_{IR}$	Key attributes: anode cathode ratio, shielding and thieves
Plating Current Density	$\Delta\eta_{mt} E_{IR}$	Lower current density enhances uniformity
Waveform		



Critical Factors for Thickness Control

<i>Factor</i>	<i>Influence</i>	<i>Comments</i>
Plating bath additives	$\Delta\eta_{ct} \Delta\eta_{mt}$	Minimize differences in plating rate vs. current density
Substrate Resistance	E_{IR}	
Metal Pattern	$E_{IR} \Delta\eta_{ct}$	



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Critical Factors for Thickness Control- Electrolyte Conductivity

- $E_{IR} = E_{IR} \text{ (electrolyte)} + E_{IR} \text{ (substrate resistance)}$
- Higher Electrolyte conductivity κ lowers E_{IR} (electrolyte)
- $E_{IR} \text{ (electrolyte)} = C i(\text{A/cm}^2) / \kappa$
- Less position dependence than substrate resistance term



Critical Factors for Thickness Control: Electrolyte Flow

- Lateral flow across substrates is critical to decrease $\Delta\eta_{mt}$
- Solution flow normal to substrates may disrupt effective via
- Solution flow vs. substrate movement is required



Critical Factor for Thickness Control- Cell Configuration

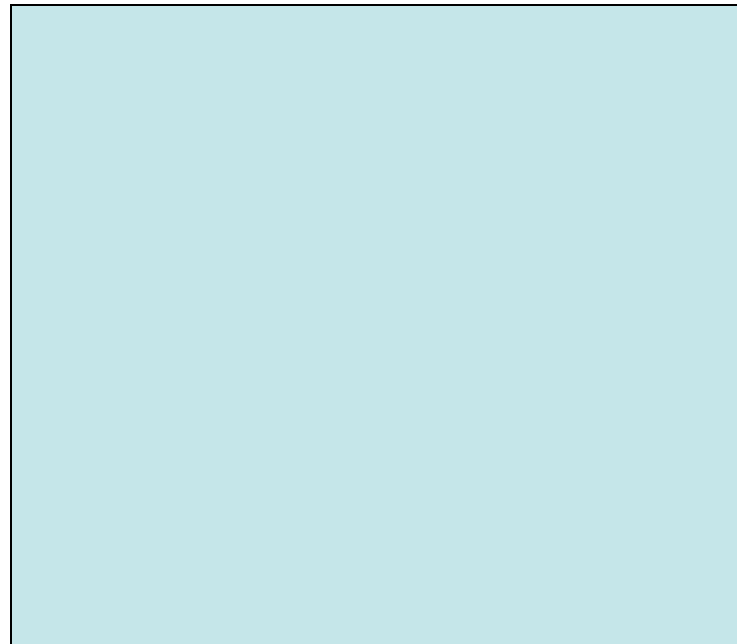
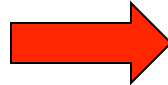
- Thieves and shielding
 - Electric potential distribution
 - Rack construction materials are critical
 - High conductivity metals (Cu)
 - Stainless steel structures are high resistivity (problem)
 - Electrochemical screening
- Anode to Cathode Ratio



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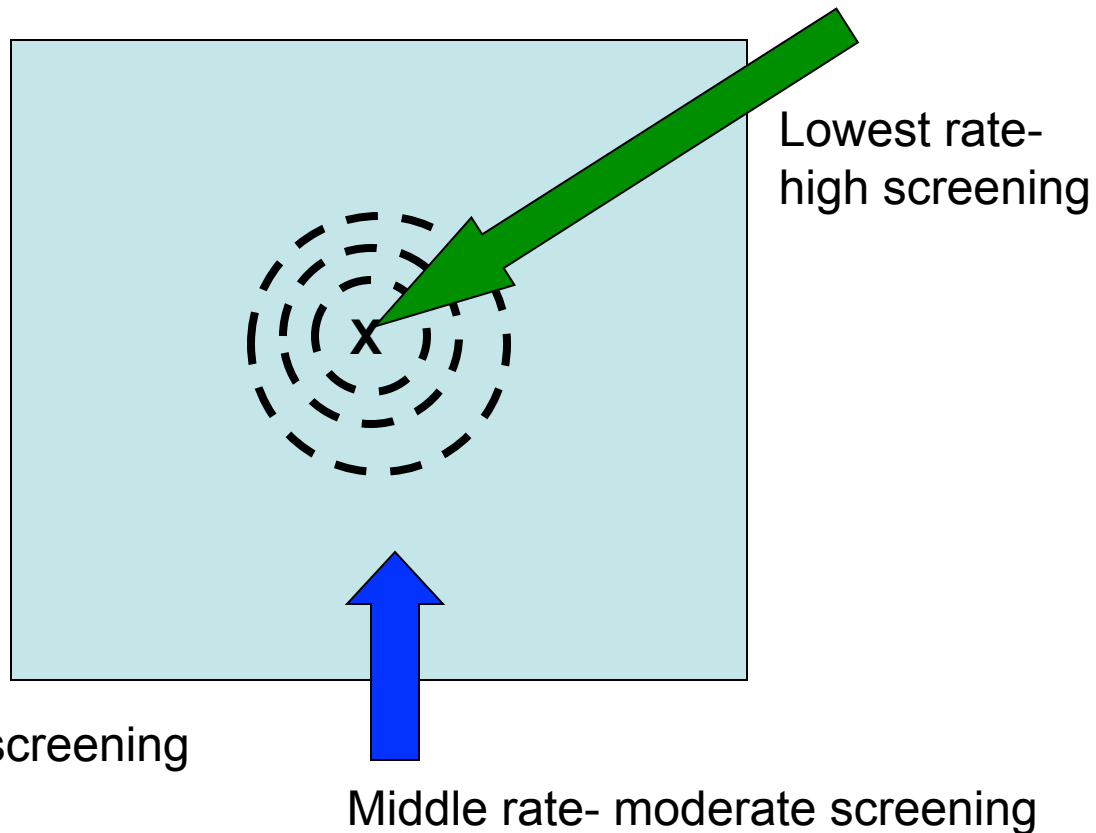
Critical Factor for Thickness Control: Role of Shielding

1. Edge and corner,
higher electric fields
→ Higher potential
means higher
plating rate



Critical Factor for Thickness Control: Role of Shielding

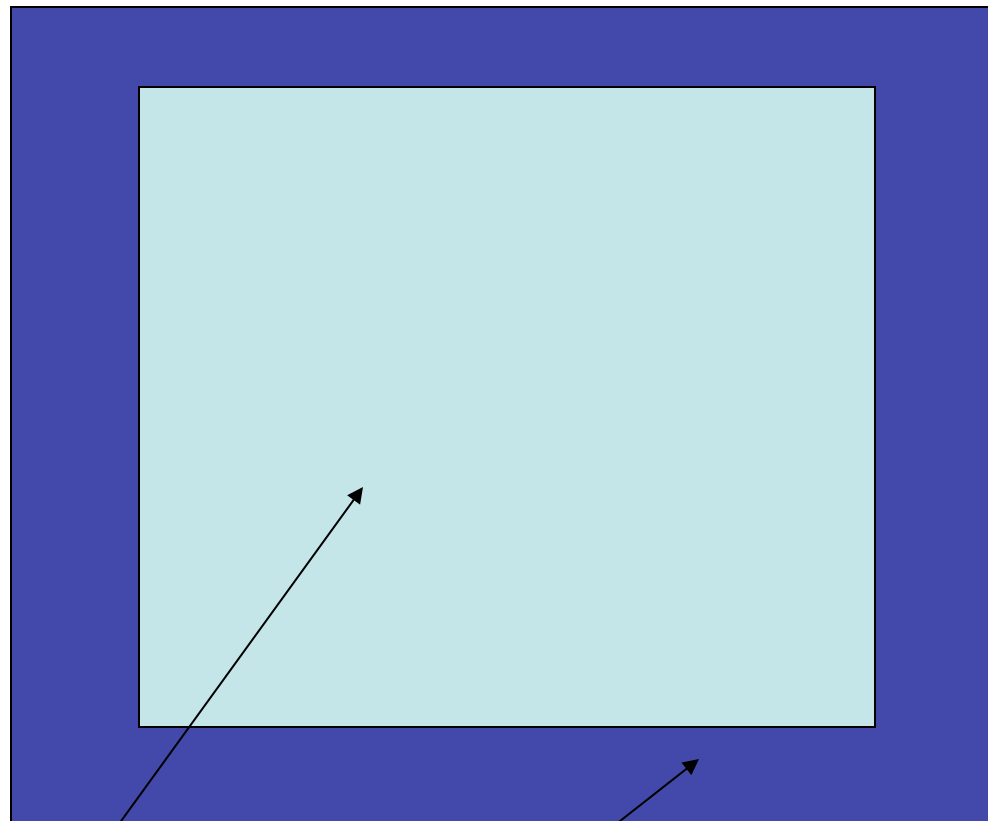
2. The plating rate at the center of the rack is reduced because concentric reaction rings around the center consume metal ions leaving lower ion concentration for the center of the rack



Critical Factor for Thickness Control: Role of Shielding

Metal screen around
the edge of the rack

1. Shifts the high electric field concentration to the edge of the shield from the edge of the rack
2. Moderates the screening effects except at the very center region



Rack

Shield



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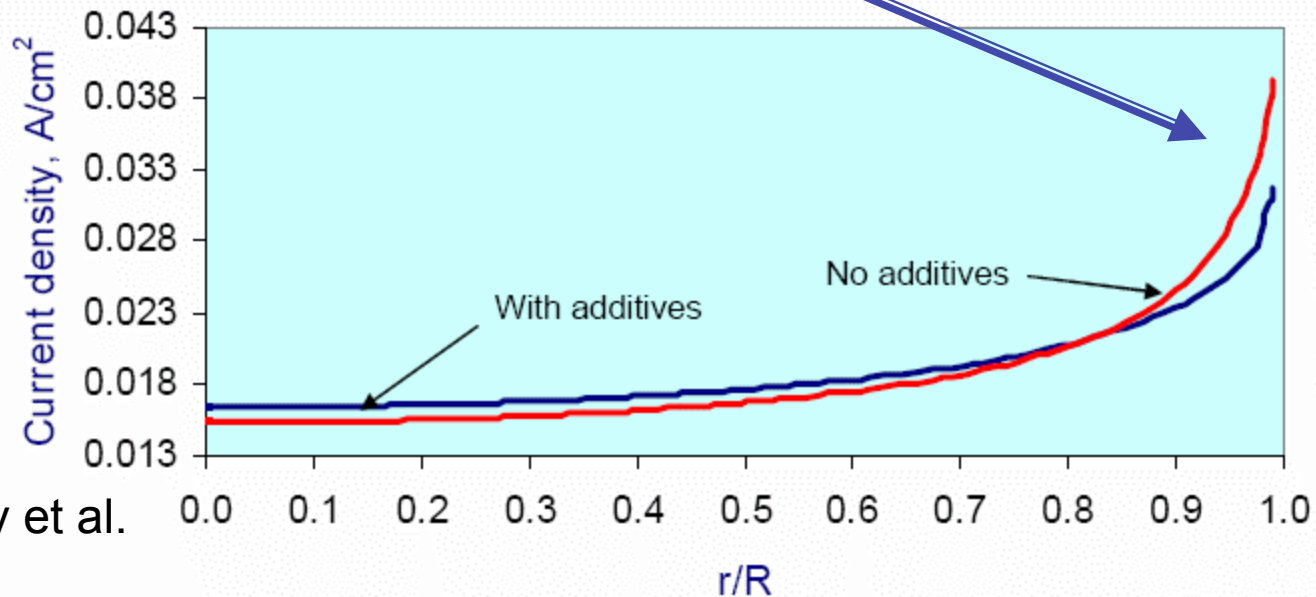
Critical Factors for Thickness Control- Plating Bath Additives

- Key role played by organic additives (levelers)
- Attach to high current density regions
 - edges
 - sharp features
- Inhibit charge transfer at high current density regions and thus lower $\Delta\eta_{ct}$



Critical Factors for Thickness Control- Plating Bath Additives

Key role of additives (levelers) at lowering the plating thickness at the (high current density edges of racks and substrates



Mayshev et al.

Red line is pure copper sulphate (0.5M, pH=2), blue line is the same electrolyte with 70 ppm Cl, 50 ppm SPS and 200 ppm Polyethylene Glycol

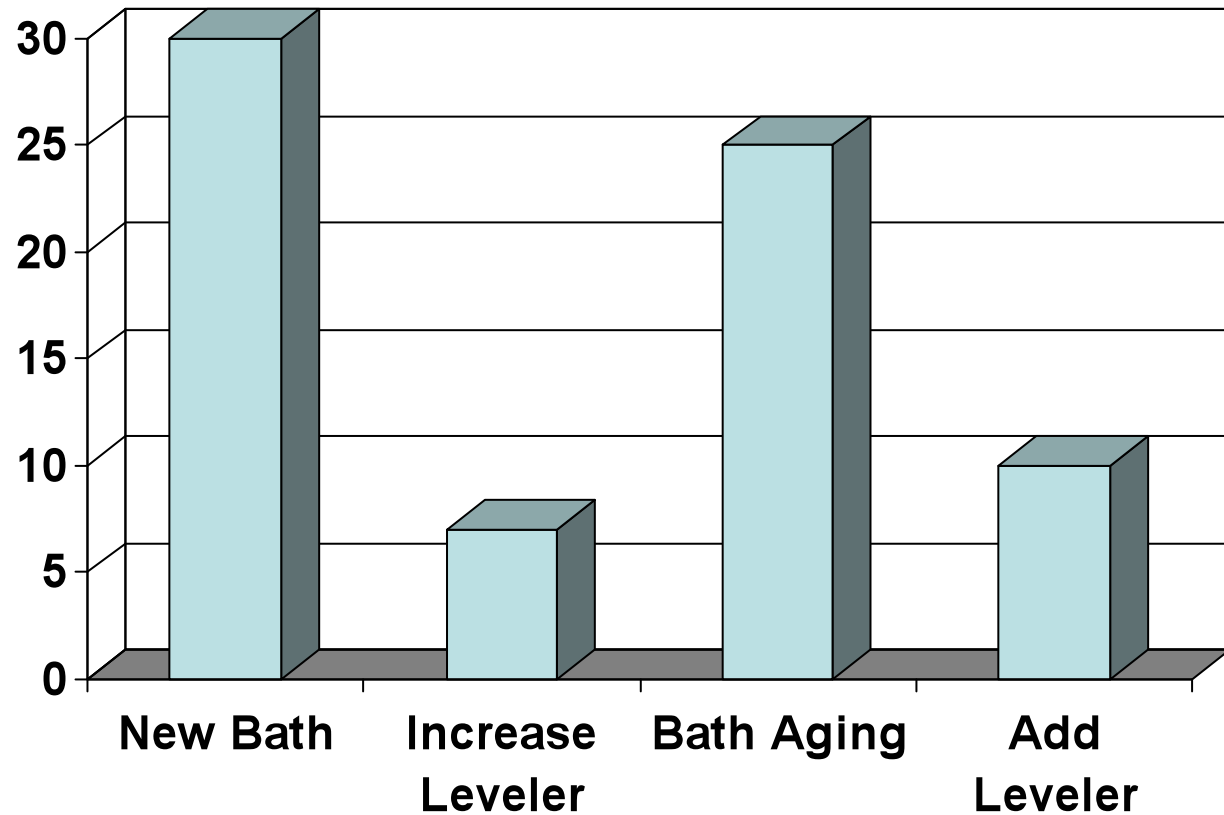
Levelers- bond to high current density regions and lower the plating rate (inhibit charge transfer)



Critical Factors for Thickness Control- Plating Bath Additives

Dumas et al.

Non
Uniformity %
(Wafer)



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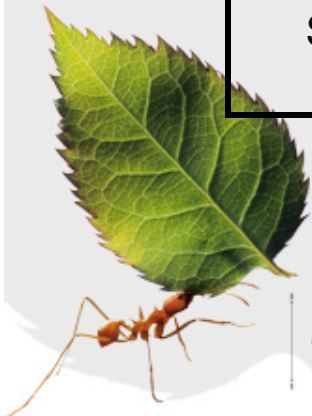
Critical Factors for Thickness Control- Current Density

- Lowering current density lowers E_{IR} which strongly depends on position along the substrate
- Plating rate limited by charge transfer kinetics η_{ct} which is independent of location ($\Delta\eta_{ct}$ is very small)



Critical Factors for Thickness Control- Trade-offs

<i>Factor</i>	<i>Trade-off</i>
Metal Concentration	Lower current densities/ lower plating throughput
Cell Configuration and Rack Design	Plating bath loading is lowered by utilization of shielding (thieves)
Plating Current Density	Lower current densities will decrease plating throughput
Waveform	Optimizing uniformity may impact other key properties such as via fill effectiveness
Plating bath additives	Increased bath maintenance (organics additives and carbon treatment of by-products)
Substrate Resistance	Sputter cost if a thicker seed layer is required. More undercut possible during etching



Maximize Via Fill and Thickness Uniformity (Cu Example)

- Organic package designed for optimum via fill
- PPR Plating Cycle
- High H₂SO₄/Cu ratio
- High H₂SO₄
- Good lateral flow across substrate
- Minimum flow into substrates
- Lowest practical current density
- Thick seed layer- especially in-via metallization



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Copper Plating



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- Role of Copper plated layer or plated via
 - Electrical conductor
 - Thermal conductor
 - Ductile layer (mediate TCE mismatch)
- Typical Key Properties
 - Thick deposit
 - Dense via fill to avoid trapping plating impurities



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Standard Copper Plating Baths

<i>Bath</i>	<i>Properties</i>	<i>Main Applications</i>
Copper Cyanide	High adhesion strike, very rapid plating	Wire plating, thick copper
Copper Pyrophosphate	Ductile, high throwing power	Electroforming, plating on plastic, PCB
Copper Sulphate	High throwing power, economical, few waste treatment issues	PCB, Electronics, semiconductors
Copper Fluoborate	High plating speed	PCB, electronics, plating on plastic, electroforming



- Copper sulfate, sulfuric acid, chloride, organic additives
- Anode/cathode efficiencies 100%
- Current densities 15-30 ASF (typical)
- For high throw applications:
 - Copper sulfate 60-75 g/liter
 - Sulfuric acid 187-225 g/liter
 - Chloride 50-100 ppm
 - Organic additives (per bath vendor)
 - Metal/Acid ratio 1:30
 - PPRP Cycle



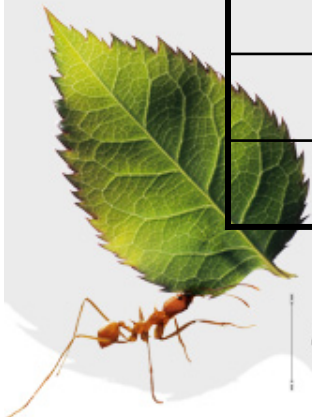
- Organics from decomposition of brighteners, resist
 - Green colored bath
 - Decrease in deposit ductility
 - Non-uniform surface morphology and grain structure
 - Nodules
- Anode bag contamination (incomplete leaching)
- Drag-in of previous chemistries
- Tank liner degradation

Carbon treatment to remove organic contamination is critical



Copper Sulfate Troubleshooting

<i>Issue</i>	<i>Cause</i>	<i>Corrective Action</i>
Burning of Deposit	Low Cu, High Acid	Analyze and correct
	Low temperature	Adjust temperature
	Insufficient agitation	Lower current or increase agitation
	Organic contamination	Carbon treat
Rough Deposit	Low Chloride	Analyze and correct
	Particles in solution	Increase filtering
	Torn anode bags	Replace
	Improper anodes	Phosphorized copper
	Low chloride	Analyze and correct



<i>Issue</i>	<i>Cause</i>	<i>Corrective Action</i>
Pitting	Low chloride	Analyze and correct
	Organic contamination	Carbon treatment
Poor Distribution	Low or non-uniform agitation	Improve agitation
	Excessive anode area	Decrease anode area
	Improper anode film	Dummy plate
	Current supply AC ripple	Adjust to <<5%
	Organic contamination	Carbon Treatment

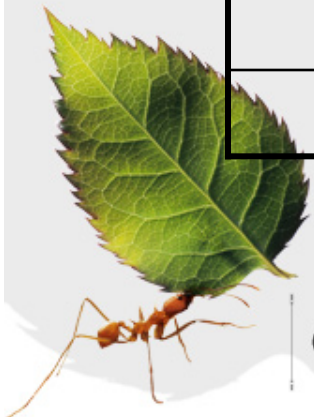


- Substrate cleaning- residue from resist developer (source of bath contamination)
- Filter bath with 1 micron filter, 5 turnovers/hr
- Minimize all sources of particulates
 - corrosion
 - dust
 - anode bag
 - Rack contacts
- Excessive current density
- Excessive brightener concentration
- Leach tanks (when a new bath is installed)
- Batch carbon treatment with activated carbon (every 3 months)



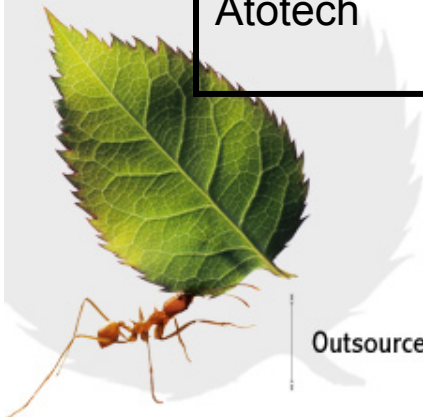
Copper Sulfate Troubleshooting

<i>Issue</i>	<i>Cause</i>	<i>Corrective Action</i>
Anode Polarization	Tin or gold contamination	Dummy plate
	Low temperature	Adjust temperature
	Improper anodes	Phosphorized anodes
	High Chloride	Clean anode film, dummy to reformulate, precipitate out Cl with Ag sulphate and carbon filter
	High Sulfuric acid	Dilute bath
	Low Copper sulfate	Analyze and adjust
	Organic Contamination	Carbon treatment

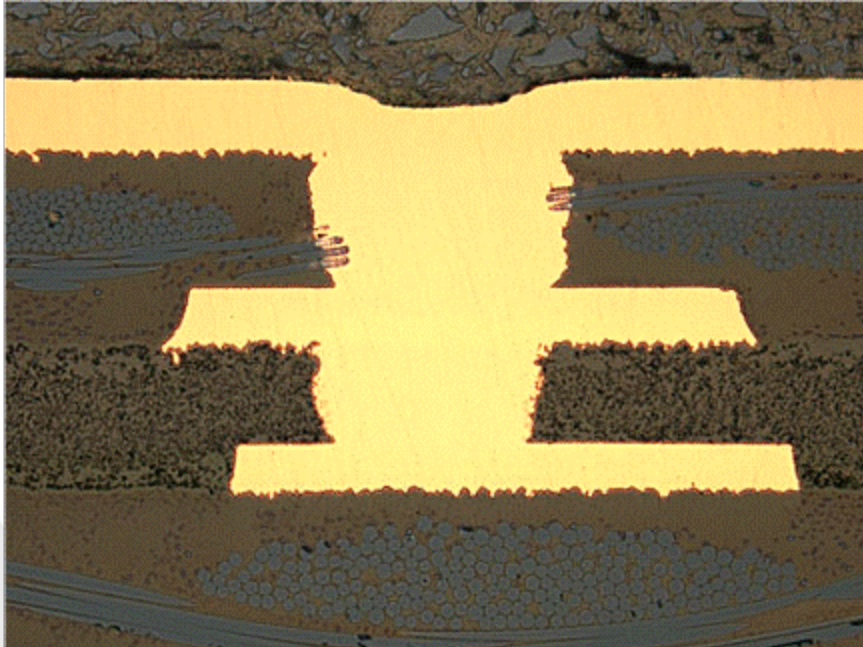


State of the Art Cu Plating Bath Suppliers

<i>Supplier</i>	<i>Bath</i>	<i>Characteristics</i>
Rohm and Haas Electronic Materials	InterVia Cu 8530	Excellent filling characteristics for micro via holes, high throwing power
Cookson Electronics	Enthone Cuprostar CVF1	Very high throwing power, optimized for DC plating, through holes and blind vias
Uyemura	Thru Cup EPL	High throwing power, optimized for DC, suppresses nodule growth.
Technics	Cu 2300	DC or PPRP compatible.
Atotech	Cupracid HLF	State of the art. Commonly used in Taiwan (good support). See next slide



State of the Art Cu Plating Bath Suppliers- Atotech



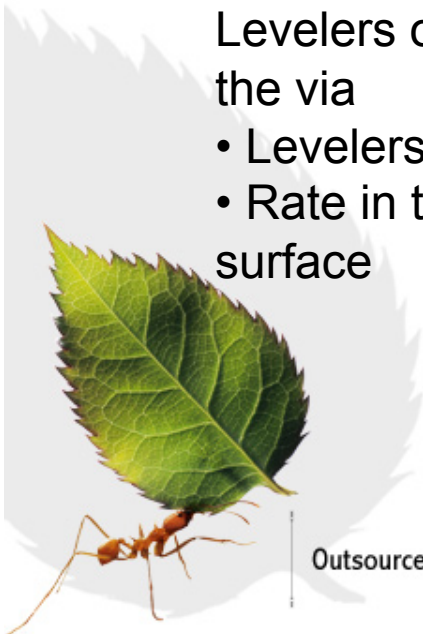
Via fill copper baths are designed to fill blind micro-vias- these baths and plating conditions may need to be optimized for open vias



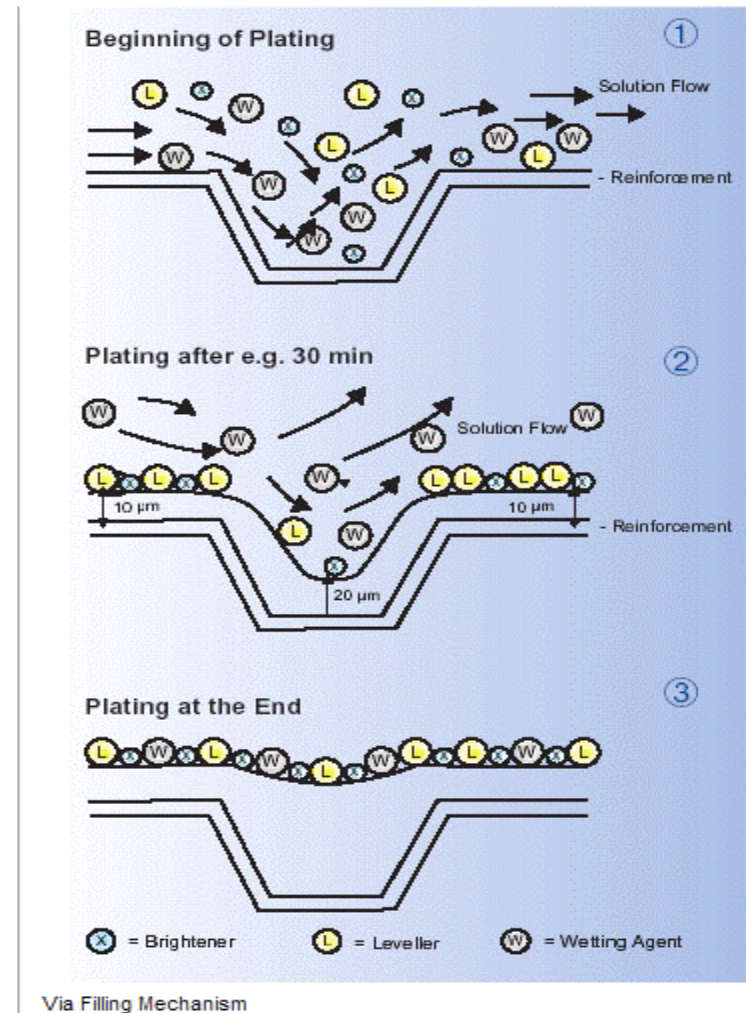
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AtoTech Cupracid Via Fill Mechanism

- Similar to the process outlined in the “Bath Components” Section
- Levelers are strongly attached to the plating surface
- Levelers are depleted in the via, but are refreshed due to solution flow on the surface → Higher concentration of Levelers on the surface versus inside the via
- Levelers inhibit plating rate
- Rate in the via is higher than the surface



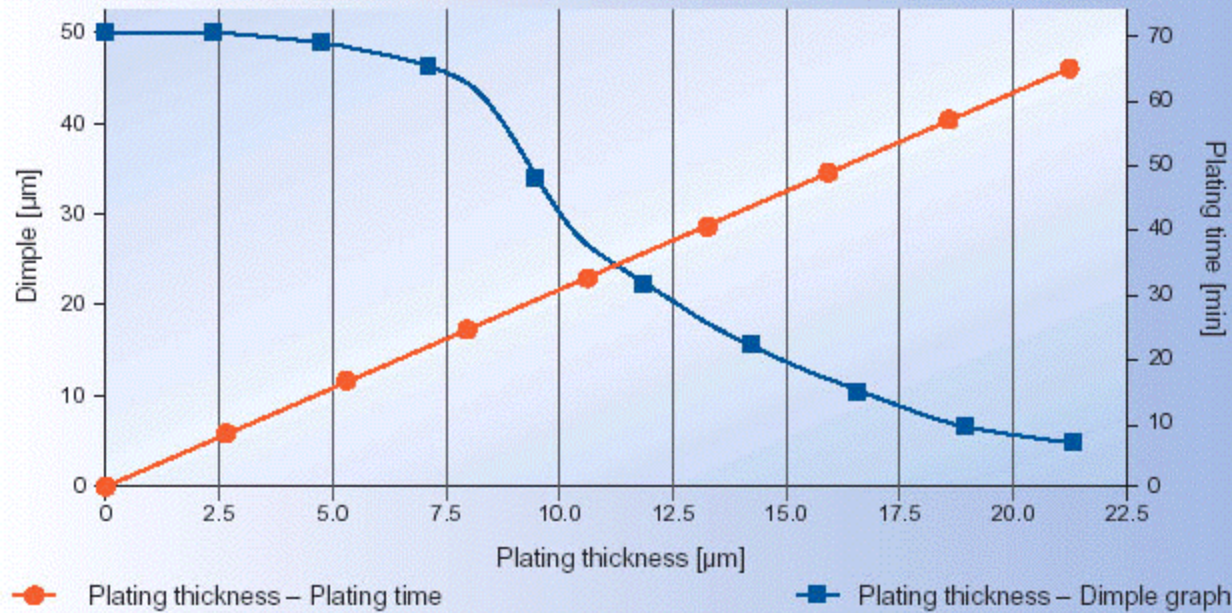
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State of the Art Cu Plating Bath Suppliers- Atotech

Plating Thickness / Dimple Matrix

BMV diameter 100 μm , depth 50 μm , $i = 0.8 \text{ A/dm}^2$



The via filling process, represented by a declining dimple, is not linear – speed increases at the beginning and slows down towards the end.



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Nickel Plating



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- Role of Nickel Layers
 - Surface for wirebond and die attach
 - Diffusion barrier for underlying metal layers (Cu, Ti)
- Typical Key Requirements
 - Nickel is not oxidized
 - Nickel layer is less than 400 micro-inches
 - Thermal barrier
 - Stress in thick nickel layers
- Ultra Smooth Reflective Leveling Nickel
 - CMC developed at Lumileds Request
 - In a separate presentation



Ni Plating Baths- Typical Compositions

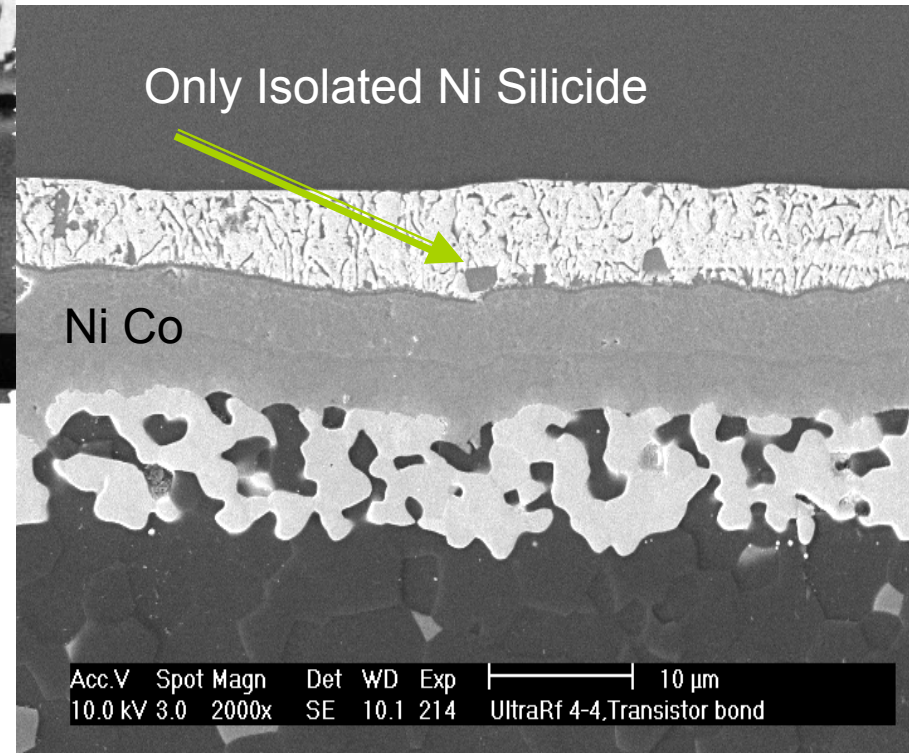
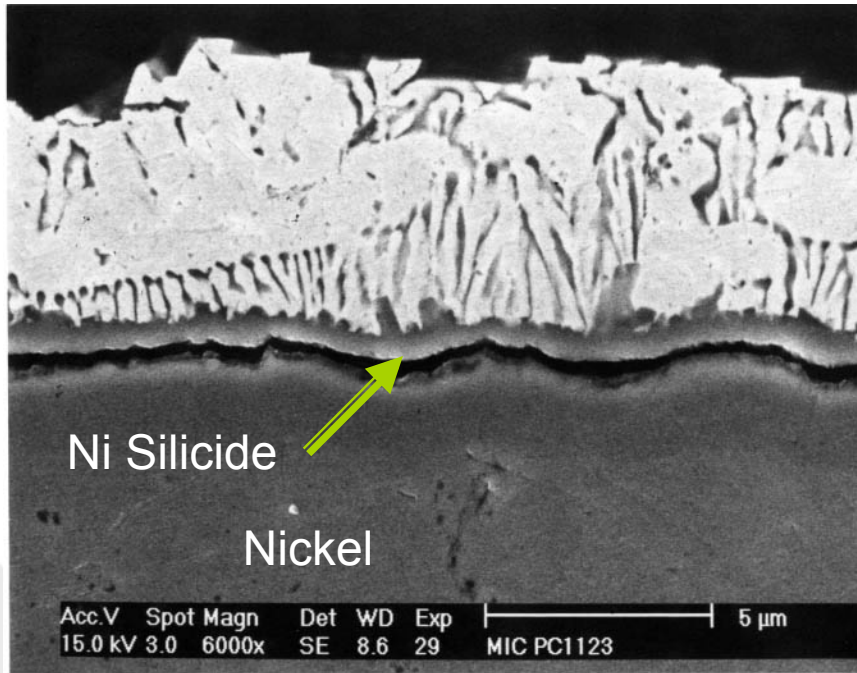
<i>Additive</i>	<i>Watts Nickel Bath (g/L)</i>	<i>Nickel Sulfamate Bath (g/L)</i>
NiSO_4	225-300	
$\text{Ni}(\text{SO}_3\text{NH}_2)_2$		315-450
NiCl_2	37-53	0-22
H_3BO_3	30-45	30-45



- Ni plated layers are very susceptible to intermetallic compound formation during solder or die attach
 - Nickel Silicide
 - Ni_3Sn_4
- IMC layers cause reliability issues
- Doping the Ni plating bath with Co frustrates Ni based IMC formation
- 10-40% Co in the deposit



Nickel Plating- IMC Issues



Outsource Your Problems to CMC Interconnect Technologies

Gold Plating



Outsource Your Problems to CMC Interconnect Technologies

- Role of Gold Layer
 - Protect underlying nickel layer from oxidation
 - Facilitate die attach and wirebonding
- Typical Key Requirements
 - Gold layer is as thin as possible to reduce cost
 - 80-100 micro inches for AuSi die attach
 - 40 micro inches for AuSn die attach
 - 40 micro inches or less for wirebonding
 - Gold layer is 99.9% pure (free of both inorganic impurities and organics)

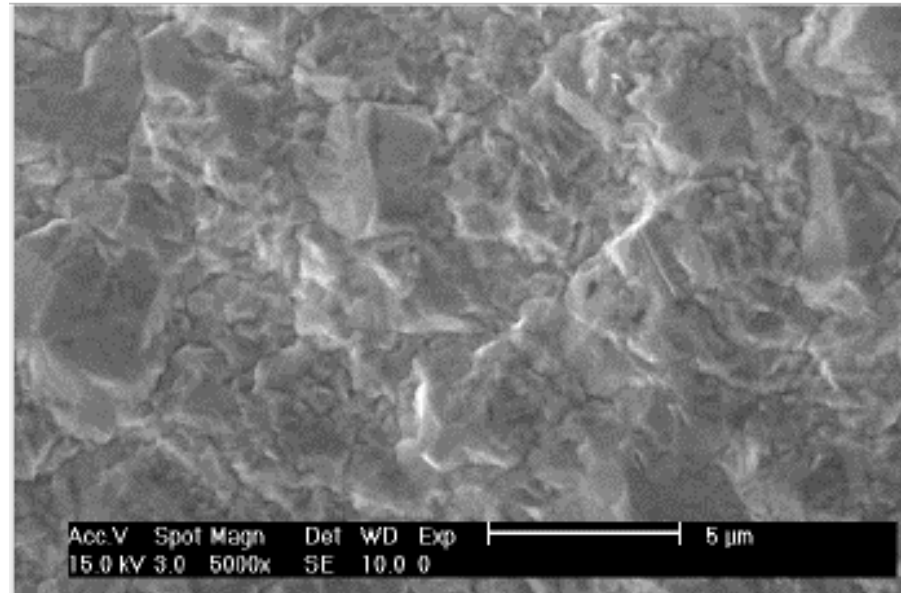


Au Plating- Die Attach and Wirebond Applications

- Au Cyanide bath (4.4-5.5)
- Potassium gold cyanide
- Proprietary grain refiners (Pb, TI)
- Deposit 99.9% pure
- High sensitivity to organics
 - Cyanide breakdown
 - Resist breakdown
 - Drag in
- PPM sensitivity to metal contamination
 - Nickel
 - Iron
- Pulse plating cycle to control grain size



Au Grain- Large Grain Size



- Gold concentration and pulse cycle are optimized to produce large “amorphous” grain structure
- Ni migration along Au grain boundaries are minimized
- Enhances GGI and wirebond characteristics
- 40 micro-inches or less for optimum grain structure



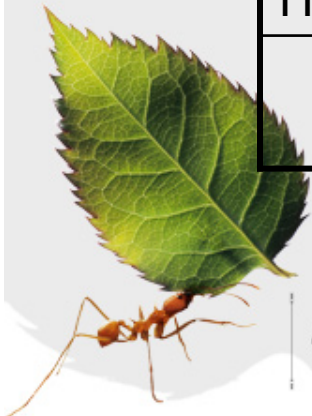
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- Organic removal through periodic carbon treatment is critical
- Gold hardness measurements to determine Ni contamination level (Coupon)
- Maintenance of the Au Strike to keep Au plating bath clean
- Monitor Pulse Plate rectifier noise profile



Au Plating Troubleshooting

<i>Issue</i>	<i>Cause</i>	<i>Corrective Action</i>
Orange color gold deposit	Organic contamination, low gold concentration	Carbon treatment, add gold
Low GGI ball shear or wirebond failure	Organic contamination	Carbon treatment
	Nickel contamination	
Gold darkens after 300C, 5 minute Hotplate test	Au layer is too thin	Re-plate for longer period
	Nickel contamination	



Measurements and Techniques to Control Plating



Outsource Your Problems to CMC Interconnect Technologies

Plating Bath Control

<i>Technique</i>	<i>What is Measured</i>	<i>Example</i>
Titration	Concentration of a reactant in solution	Cu concentration in a Cu Sulfate bath
Atomic Absorption	Dissolved metals in a plating solution	Identifying and quantifying metallic impurities
Hull Cell	Deposit quality versus current density	Monitoring organic brightener
CVS	Active organic components in a bath	Brighteners or carriers in a copper plating bath
Wetting Angle	Wetting characteristics of the bath.	Measures surfactant levels. Key to via fill and bubble elimination.
Selective ion (Cl, pH)	Activity of an active ion such as H ⁺ , OH ⁻ or Cl	Monitor Cl concentration in a copper sulfate bath
Hardness Testing	Hardness of a plated layer	Excellent for detecting small levels of impurities in gold
X ray fluorescence	Major bath constituents or plated layer thickness	Gold or nickel concentration

Hull Cell Measurements- Process Control Tool

- Optimization for current density range
 - Optimum deposit characteristics at chosen current density
 - Bath optimization for widest current density range (improves thickness uniformity, via fill, feature fill)
- Optimization of additive concentration
- Recognition of impurity effects
 - Organic
 - In-organic
- Indication of throwing power capability

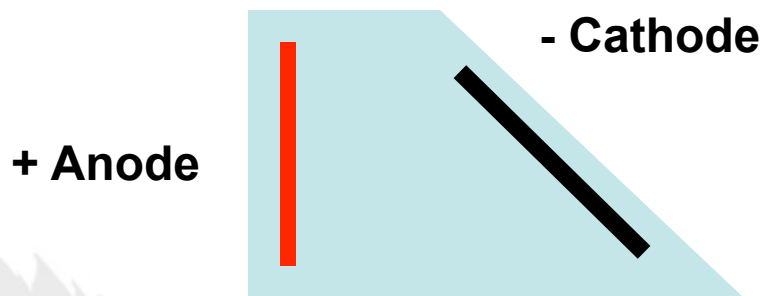


Hull Cell Measurements: Process Development and Control Tool

- Utilize Hull Cell measurements to optimize bath additive composition (organics) during process development
 - Maximize current density range
 - Maximize leveling
 - Maximize brightness/ reflectivity
- Use routine Hull Cell Measurement to monitor changes in bath chemistry
 - Impurities
 - Organic additive depletion
 - Mechanical properties of the deposit (stress)



2A through cell translates to a current density range .25 to 10 A/dm²



Top View

Cell Volume= 267 ml
2 g addition → 2 oz/ gallon in plating bath



Hull Cell Measurement Procedure: Metallic Impurities

1. Insure that plating bath parameters such as pH, metal concentration, acid concentration are within specified limits
2. Take a sample of the plating bath chemistry and transfer to the Hull Cell and plate
3. Look for a dark deposit at low current density regions indicating metallic contamination
4. If a contaminant is present, analyze with AA to identify the source (note that the concentration in the deposit can be much higher than in the bath)
5. Remove the contaminant with ion exchange and/or dummy plating
6. Repeat Hull Cell measurement to insure dark deposit is no longer present



Hull Cell Measurement Procedure: Organic Impurities

1. Take a sample of the plating bath chemistry and transfer to the Hull Cell and plate
2. A dark or black deposit near the solution level interface indicates organic or metallic contamination
3. If this is seen, carbon treat the Hull Cell solution
4. Repeat the test on a new Hull Cell panel
5. Replenish the Brightener if necessary (noting how much was added)
6. Plate another panel
7. Once the desired deposit quality is reached, perform the same carbon treatment and Brightener adds to the plating bath

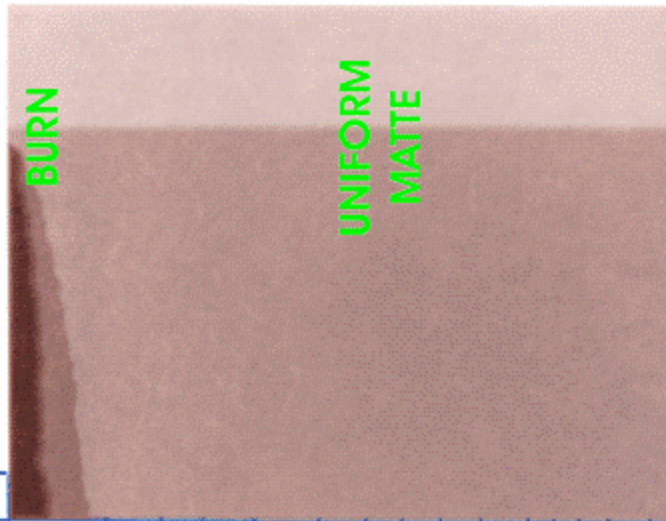


Hull Cell Measurement Procedure: Leveling Power

1. Take a representative sample of the bath and transfer into the Hull Cell
2. Plate onto a brass panel with a scratched surface
3. Inspect the deposit for smoothness
4. Adjust Leveler concentration until desired current density region on the Hull cell panel exhibits a smooth finish (no evidence of scratches)
5. Reproduce these adds in the plating bath



Hull Cell Panel Examples: Acid Cu Bath



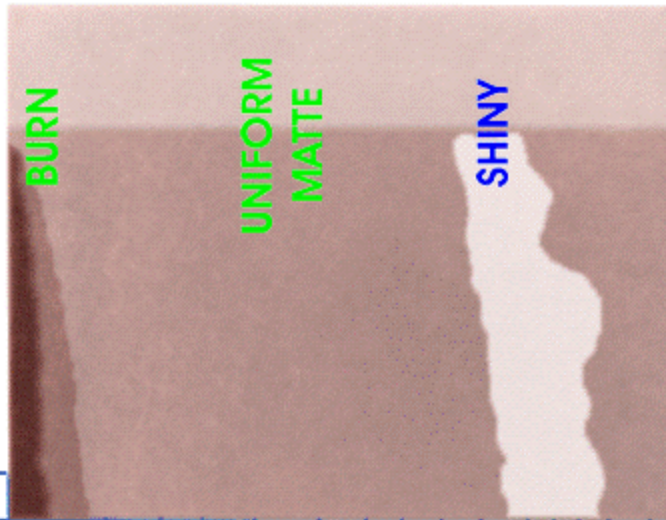
Bath after carbon treatment, no brightener

1 AMP	HULL CELL SCALE														
2 AMPS.	80	60	50	40	30	24	20	16	12	8	6	4	2	1	
TOTAL CURRENT	AMPS./SQ. FT.—267 ML. or 534 ML. HULL CELL 2 GM/267 ML. HULL CELL = 1 OZ./GAL. = 6.25 LBS./100 GAL. 2 ML/267 ML. HULL CELL = 0.96 FL. OZ./GAL. = 6 PTS./100 GAL. 4 GM/534 ML. HULL CELL = 1 OZ./GAL. = 6.25 LBS./100 GAL. 4ML/534 ML. HULL CELL = 0.96 FL. OZ./GAL. = 6 PTS./100 GAL.														
3 AMPS.	120	90	75	60	45	36	30	24	18	12	9	6	3	15	
5 AMPS.	200	150	125	100	75	60	50	40	30	20	15	10	5	25	



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Hull Cell Panel Examples: Acid Cu Bath



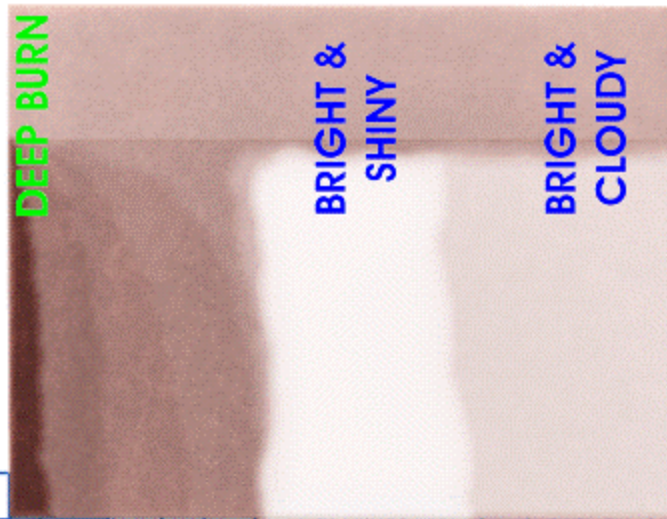
Bath with residual organic contamination exhibiting a shiny region at low current densities

1 AMP	HULL CELL SCALE													
2 AMPS.	80	60	50	40	30	24	20	16	12	8	6	4	2	1
TOTAL CURRENT	AMPS./SQ. FT.—267 ML. or 534 ML. HULL CELL 2 GM/267 ML. HULL CELL = 1 OZ./GAL. = 6.25 LBS./100 GAL. 2 ML/267 ML. HULL CELL = 0.96 FL. OZ./GAL. = 6 PTS./100 GAL. 4 GM/534 ML. HULL CELL = 1 OZ./GAL. = 6.25 LBS./100 GAL. 4ML/534 ML. HULL CELL = 0.96 FL. OZ./GAL. = 6 PTS./100 GAL.													
3 AMPS.	120	90	75	60	45	36	30	24	18	12	9	6	3	15
5 AMPS.	200	150	125	100	75	60	50	40	30	20	15	10	5	25



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Hull Cell Panel Examples: Acid Cu Bath



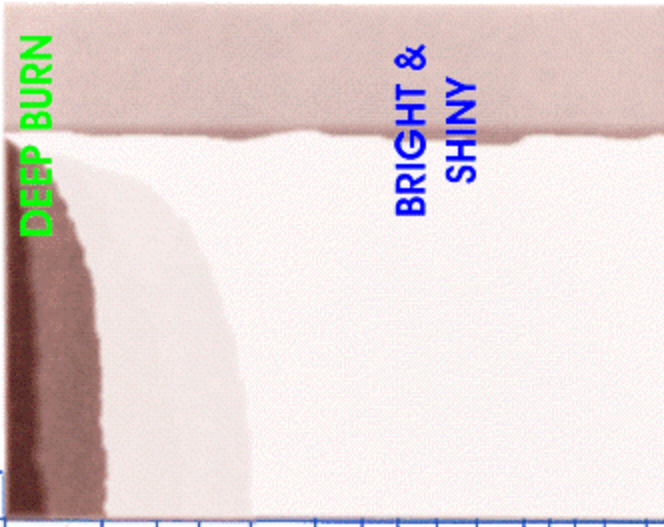
Balanced electrolyte with Brightener- broad region of shiny surface 12-40 ASF

1 AMP	HULL CELL SCALE													
2 AMPS.	80	60	50	40	30	24	20	16	12	8	6	4	2	1
TOTAL CURRENT	AMPS./SQ. FT.—267 ML. or 534 ML. HULL CELL 2 GM/267 ML. HULL CELL = 1 OZ./GAL. = 6.25 LBS./100 GAL. 2 ML/267 ML. HULL CELL = 0.96 FL. OZ./GAL. = 6 PTS./100 GAL. 4 GM/534 ML. HULL CELL = 1 OZ./GAL. = 6.25 LBS./100 GAL. 4ML/534 ML. HULL CELL = 0.96 FL. OZ./GAL. = 6 PTS./100 GAL.													
3 AMPS.	120	90	75	60	45	36	30	24	18	12	9	6	3	15
5 AMPS.	200	150	125	100	75	60	50	40	30	20	15	10	5	25



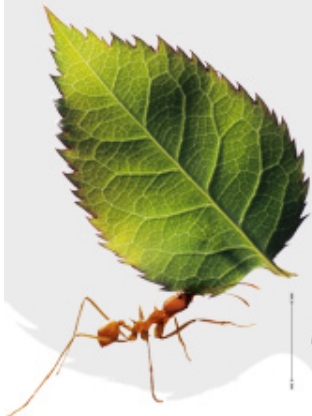
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Hull Cell Panel Examples: Acid Cu Bath



Balanced electrolyte high acid with Brightener- broad region of shiny surface 1-40 ASF

1 AMP	HULL CELL SCALE													
2 AMPS.	80	60	50	40	30	24	20	16	12	8	6	4	2	1
TOTAL CURRENT	AMPS./SQ. FT.—267 ML. or 534 ML. HULL CELL 2 GM/267 ML. HULL CELL = 1 OZ./GAL. = 6.25 LBS./100 GAL. 2 ML/267 ML. HULL CELL = 0.96 FL. OZ./GAL. = 6 PTS./100 GAL. 4 GM/534 ML. HULL CELL = 1 OZ./GAL. = 6.25 LBS./100 GAL. 4ML/534 ML. HULL CELL = 0.96 FL. OZ./GAL. = 6 PTS./100 GAL.													
3 AMPS.	120	90	75	60	45	36	30	24	18	12	9	6	3	15
5 AMPS.	200	150	125	100	75	60	50	40	30	20	15	10	5	25



Plating Process **Window** Characterization



Outsource Your Problems to CMC Interconnect Technologies

CMC Experience in Plating Process Development

- Critical to determine processing windows for all key plating steps using the *physical and chemical signature on the plated part*
- Establish a library of process characterization data
- Troubleshooting approach: use this library to identify problem plating steps and to develop solid leads into root-cause
- Up-front time investment in process characterization saves significant time in more directed problem solving
- Key tool in yield improvement and process optimization
- Most important for cleaning/etching/pretreatment steps.



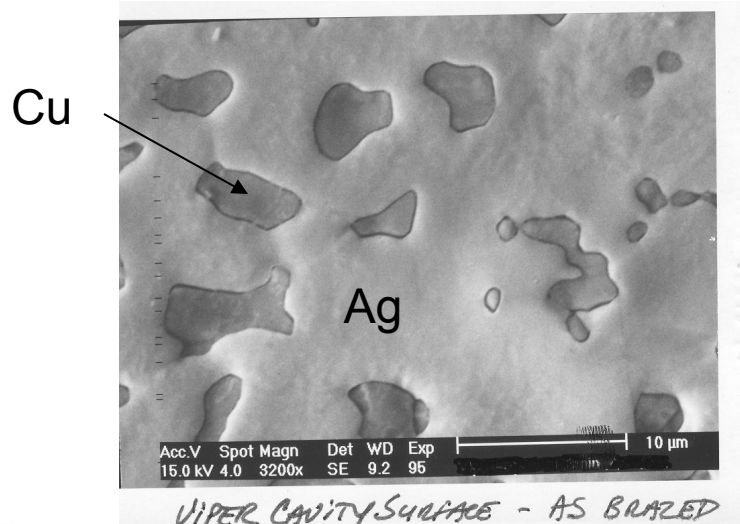
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Process Window Example: CuAg Etch Prior to Plating

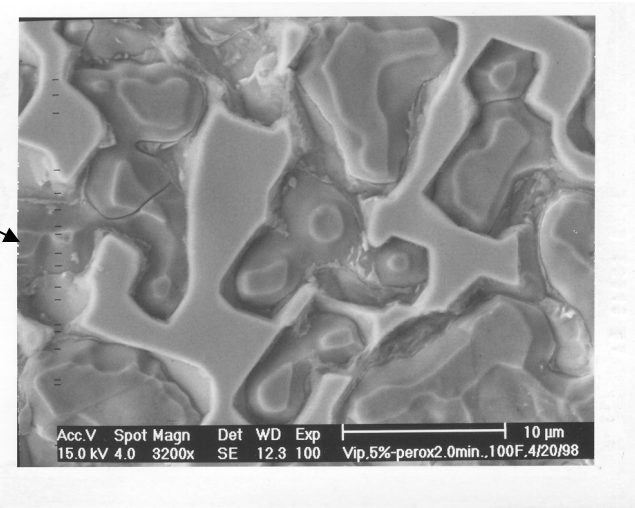
- CuAg is a high temperature braze material
- Ag is very difficult to activate and plate without blisters (due to AgCl formation)
- Goal of the CuAg etch is to produce a copper-rich surface prior to plating
- An etch mixture containing H₂O₂ (peroxide) is used
- The time in the etch is varied and the surface microstructure is established
- The goal is to determine etch time for a consistent surface chemistry



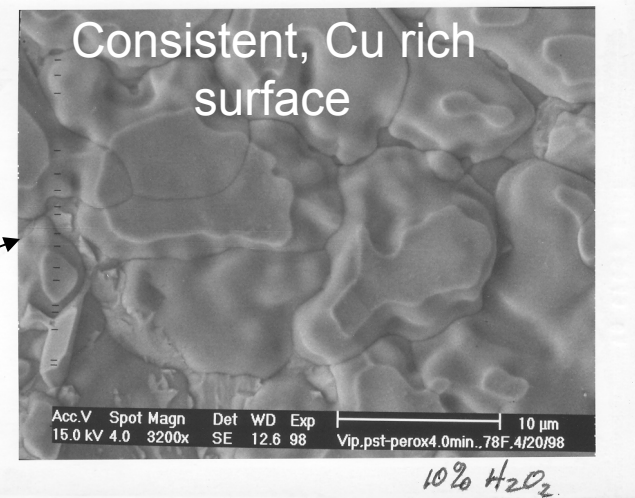
Process Window Example: CuAg Etch Prior to Plating



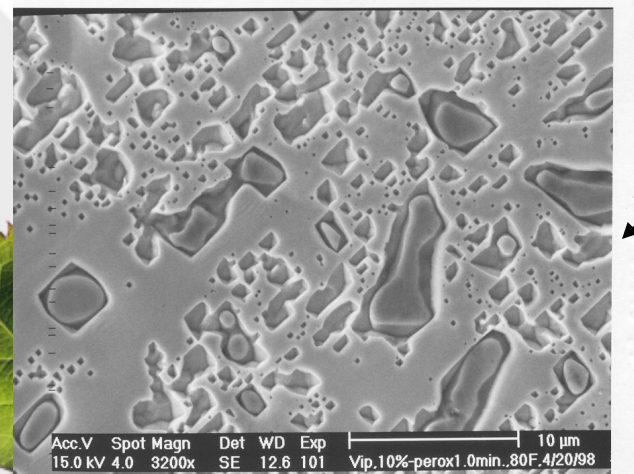
As received



2 minutes



4 minutes



1 minute

